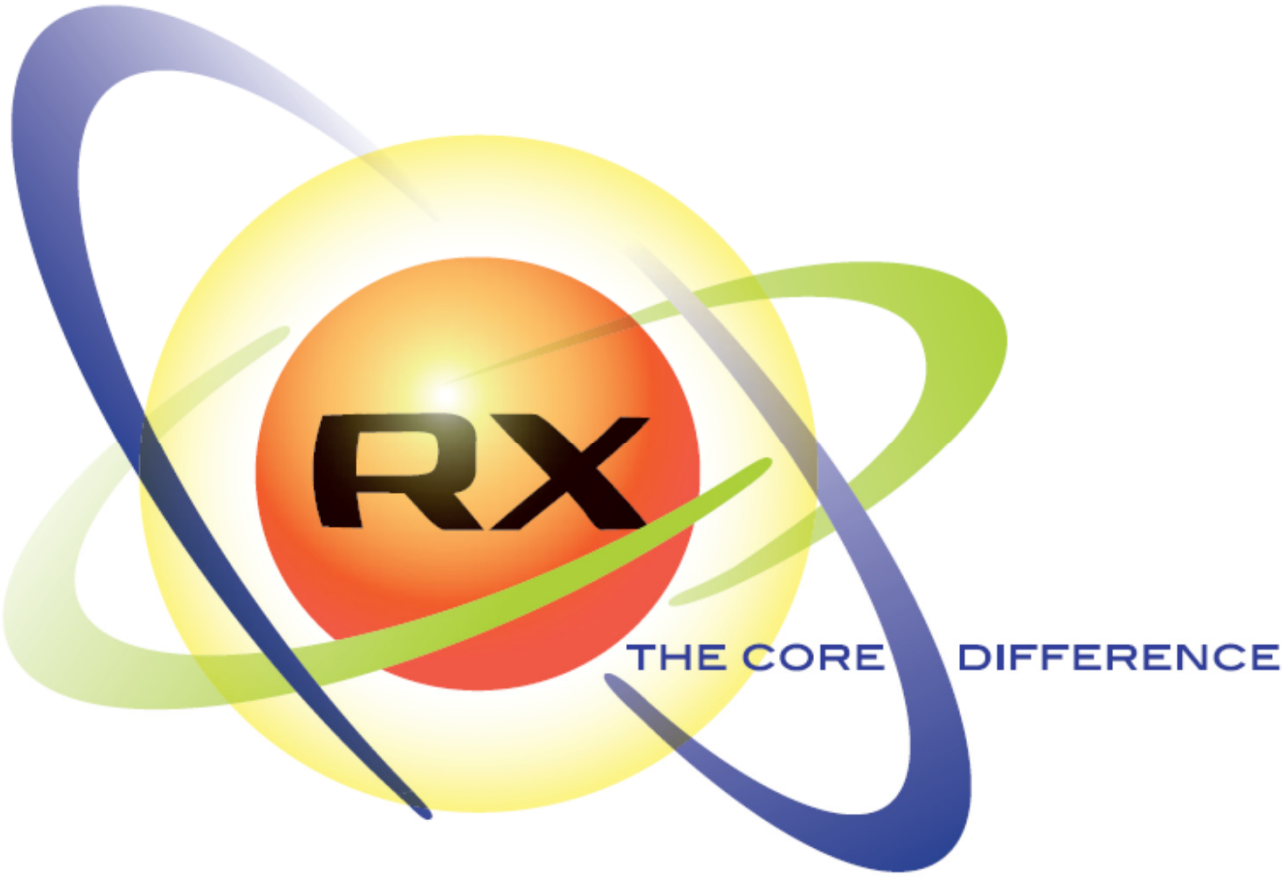
Quick Reference Guide



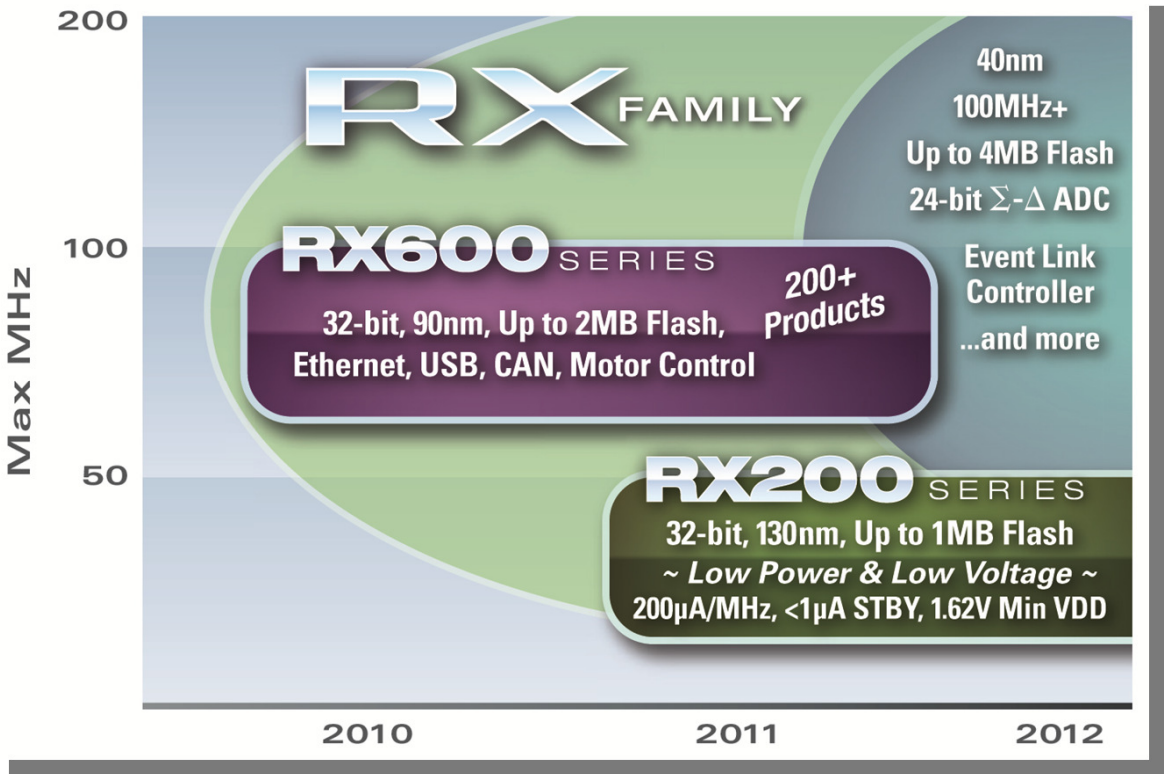
September 2012 **Version 0.74**

© 2010 Renesas Electronics Europe. All rights reserved.

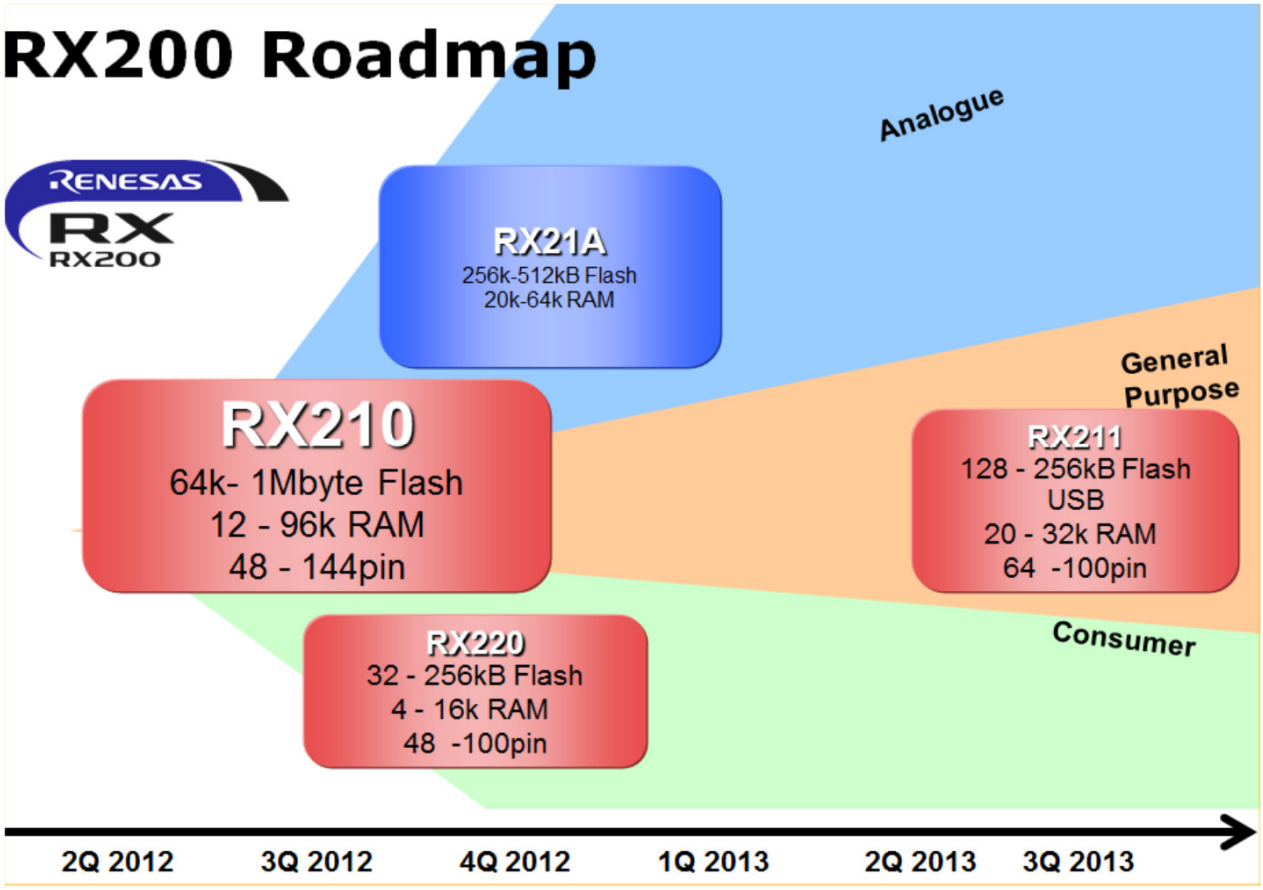
RX Series Quick Reference September-12

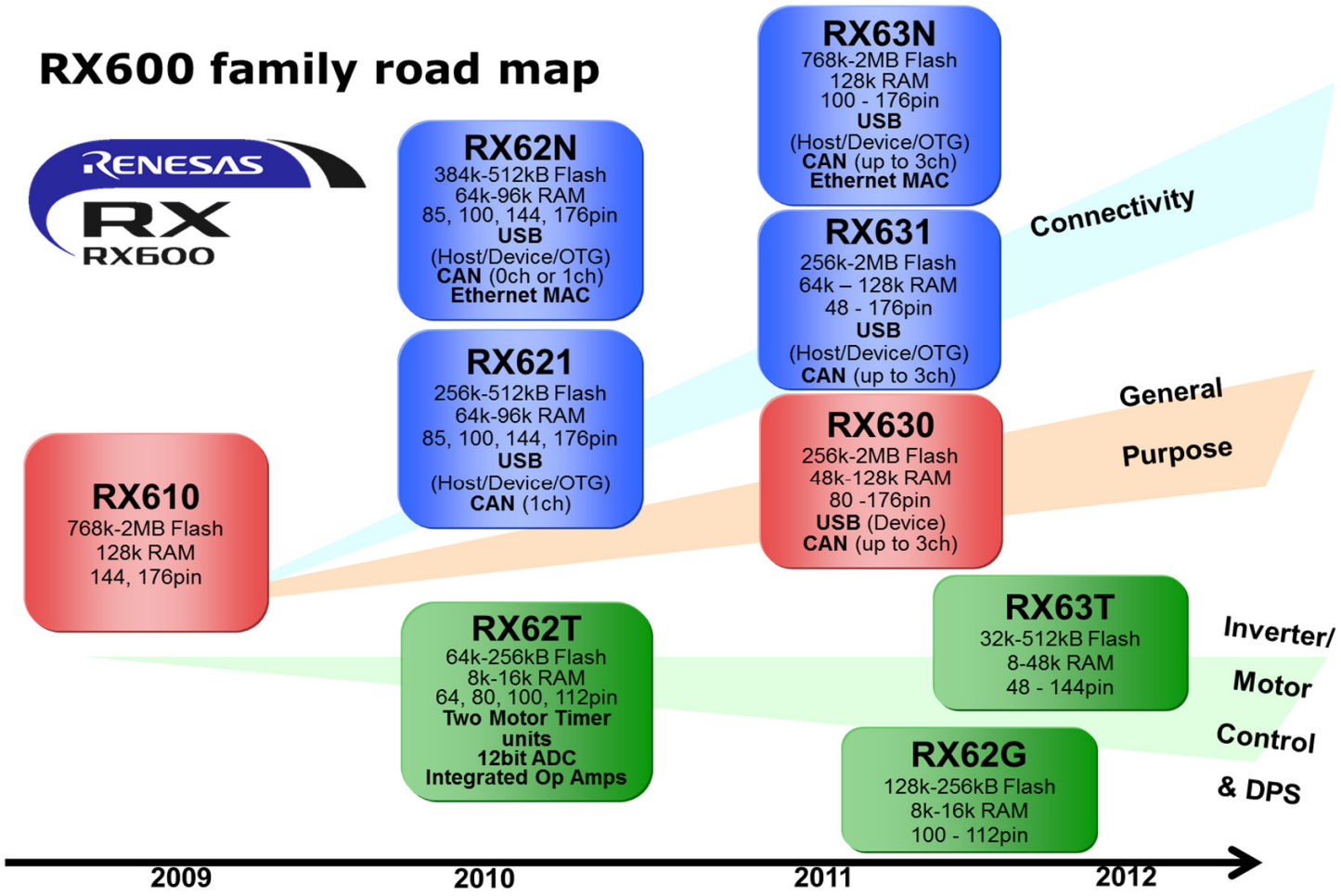
Contents

- RX Roadmap
- RX200 Roadmap
- RX600 Roadmap
- RX Promotion Status
- RX Sample Stock Snapshot
- RX200 Series part naming conventions
- RX600 Series part naming conventions
- RX Series Selector Guide
- RX Device Ordering Information & Status
  - RX610 Group
  - RX620 Group
  - RX62N / RX621 Group
  - RX62T Group
  - RX630 Group
  - RX63N / RX631 Group
  - RX63T Group
  - RX210 Group
  - RX220 Group
  - RX21A Group
- RX Users Manual Status
- RX Technical Update List
- RX Application Note List
- RX Flash Programming Information
- RX Packaging info (includes MOQ's, Tray Qty's etc)
- RX Development Tools
- RX Software Revisions
- RX Oscillator Suppliers
- RX Middleware
- RX Ecosystem


200
100
50
Max MHz
RX FAMILY
40nm
100MHz+
Up to 4MB Flash
24-bit Σ-Δ ADC
Event Link Controller
...and more
RX600 SERIES
32-bit, 90nm, Up to 2MB Flash,
Ethernet, USB, CAN, Motor Control
200+ Products
RX200 SERIES
32-bit, 130nm, Up to 1MB Flash
~ Low Power & Low Voltage ~
200µA/MHz, <1µA STBY, 1.62V Min VDD
2010
2011
2012

Renesas RX200 Micon Roadmap




RX600 family road map
RENESAS
RX
RX600
RX610
768k-2MB Flash
128k RAM
144, 176pin
RX62N
384k-512kB Flash
64k-96k RAM
85, 100, 144, 176pin
USB
(Host/Device/OTG)
CAN (0ch or 1ch)
Ethernet MAC
RX621
256k-512kB Flash
64k-96k RAM
85, 100, 144, 176pin
USB
(Host/Device/OTG)
CAN (1ch)
RX62T
64k-256kB Flash
8k-16k RAM
64, 80, 100, 112pin
Two Motor Timer units
12bit ADC
Integrated Op Amps
RX63N
768k-2MB Flash
128k RAM
100 - 176pin
USB
(Host/Device/OTG)
CAN (up to 3ch)
Ethernet MAC
RX631
256k-2MB Flash
64k – 128k RAM
48 - 176pin
USB
(Host/Device/OTG)
CAN (up to 3ch)
RX630
256k-2MB Flash
48k-128k RAM
80 -176pin
USB (Device)
CAN (up to 3ch)
RX63T
32k-512kB Flash
8-48k RAM
48 - 144pin
RX62G
128k-256kB Flash
8k-16k RAM
100 - 112pin
Connectivity
General Purpose
Inverter/ Motor Control & DPS
2009
2010
2011
2012

RX Family Quick Reference (Promotion Status) Sep-12

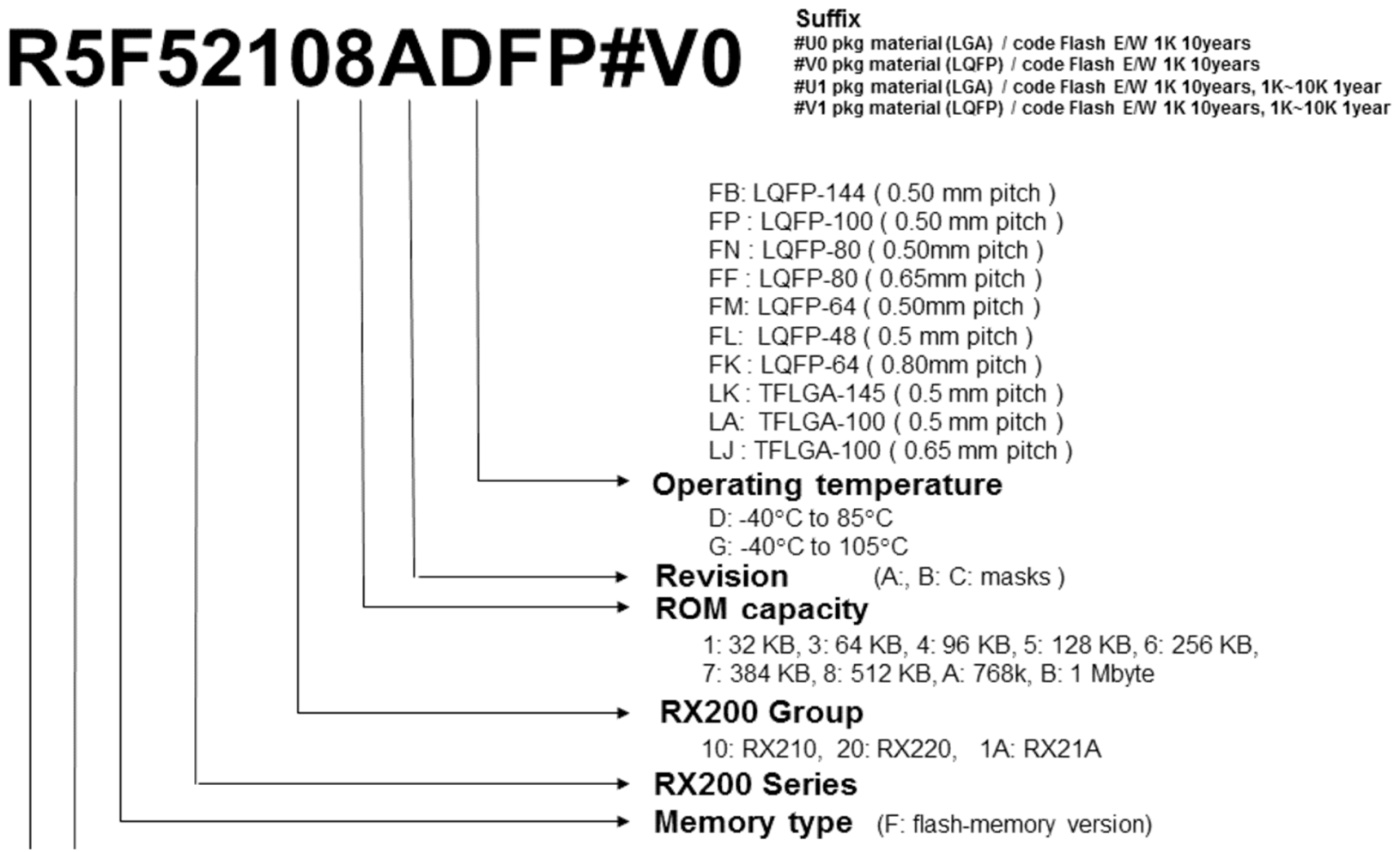
Device	Promotion Status	Device Status
RX610 Group	**Focus product**	MP
RX62N Group	**Focus product**	MP
RX621 group	**Focus product**	MP
RX62T Group	**Focus product**	MP
RX62G Group	**Future Product**	now MP Q3/2012
RX630 Group	**Focus product**	MP
RX631 Group	**Focus product**	MP
RX63N Group	**Focus product**	MP
RX63T Group	**Future Product**	Sample Q1/2012 MP Q3/2012
RX210 Group (256kbyte)	**Focus Product**	Samples Now MP 4Q 2012
RX210 Group (512kbyte) "A" revision		MP 4Q 2012
RX210 Group (512kbyte) "C" revision		
RX210 Group (1 Mbyte)		Samples Sept '12 MP 2Q 2013
RX220 Group	**Focus Product** **contact PM**	Samples Now MP 4Q 2012
RX21A Group	**Case by case promotion** **contact PM**	Samples Now MP 1Q 2013

RX Family Quick Reference (Sample Stock Snapshot) Sep-12

Device	Umbrella Device Part Number	Flash size (bytes)	Package	Comment	Sample Status	Covers the following devices
RX610	R5F56108VDFP#V0	2MB	144pin LQFP		Samples in stock	R5F56108VNFP#V0, R5F56107VDFP#V0, R5F56107VNFP#V0, R5F56106VDFP#V0, R5F56106VNFP#V0, R5F56104VDFP#V0, R5F56104VNFP#V0
	R5F56108WDBG#U0	2MB	176pin BGA		Samples in stock	R5F56108WNBG#U0, R5F56107WDBG#U0, R5F56107WNBG#U0, R5F56106WDBG#U0, R5F56106WNBG#U0, R5F56104WDBG#U0, R5F56104WNBG#U0
RX62N	R5F562N8BDFP#V0	512KB	100pin LQFP		Samples in stock	R5F562N7BDFP#V0, R5F562N8ADFP#V0, R5F562N7ADFP#V0, R5F56217BDFP#V0, R5F56216BDFP#V0
	R5F562N8BDFB#V0	512KB	144pin LQFP		Samples in stock	R5F562N7BDFB#V0, R5F562N8ADFB#V0, R5F562N7ADFB#V0, R5F56217BDFB#V0, R5F56216BDFB#V0
	R5F562N8BDLE#U0	512KB	145pin LGA		Samples in stock	R5F562N7BDLE#U0, R5F562N8ADLE#U0, R5F562N7ADLE#U0, R5F56217BDLE#U0, R5F56216BDLE#U0
	R5F562N8BDBG#U0	512KB	176pin BGA		Samples in stock	R5F562N7BDBG#U0, R5F562N8ADBG#U0, R5F562N7ADBG#U0, R5F56217BDBG#U0, R5F56216BDBG#U0
RX621	R5F56218BDLD#U0	512KB	85pin LGA		Samples in stock	R5F56217BDLD#U0, R5F56216BDLD#U0
	For all other packages, please use RX62N as sample				Samples in stock	
RX62T	R5F562TABDFM#V0	256KB	64pin LQFP	3V, CAN	Samples in stock	R5F562T7BDFM#V0,R5F562T6BDFM#V0,R5F562TAEDFM#V0, R5F562T7EDFM#V0,R5F562T6EDFM#V0
	R5F562TABDFF#V0	256KB	80pin LQFP	3V, CAN	Samples in stock	R5F562T7BDFF#V0,R5F562T6BDFF#V0, R5F562TAEDFF#V0,R5F562T7EDFF#V0, R5F562T6EDFF#V0
	R5F562TABDFP#V0	256KB	100pin LQFP	3V, CAN	Samples in stock	R5F562T7BDFP#V0, R5F562T6BDFP#V0, R5F562TAEDFP#V0,R5F562T7EDFP#V0, R5F562T6EDFP#V0
	R5F562TAADFM#V0	256KB	64pin LQFP	5V, CAN	Samples in stock	R5F562T7ADFM#V0, R5F562T6ADFM#V0, R5F562TADDFM#V0,R5F562T7DDFM#V0, R5F562T6DDFM#V0
	R5F562TAADFF#V0	256KB	80pin LQFP	5V, CAN	Samples in stock	R5F562T7ADFF#V0,R5F562T6ADFF#V0, R5F562TADDFF#V0, R5F562T7DDFF#V0, R5F562T6DDFF#V0
	R5F562TAADFP#V0	256KB	100pin LQFP	5V, CAN	Samples in stock	R5F562T7ADFP#V0,R5F562T6ADFP#V0, R5F562TADDFP#V0,R5F562T7DDFP#V0,R5F562T6DDFP#V0
RX630	R5F5630EDDFC	2MB	176pin LQFP		Contact PM	
	R5F5630EDDFB	2MB	144pin LQFP		Contact PM	
	R5F5630EDDFP	2MB	100pin LQFP		Contact PM	
	R5F5630EDDBG	2MB	176pin BGA		Contact PM	
	R5F5630EDDLK	2MB	145pin LGA		Contact PM	
RX63N	R5F563NEDDFC	2MB	176pin LQFP		Contact PM	
	R5F563NEDDFB	2MB	144pin LQFP		Contact PM	
	R5F563NEDDFP	2MB	100pin LQFP		Contact PM	
	R5F563NEDDBG	2MB	176pin BGA		Contact PM	
	R5F563NEDDLK	2MB	145pin LGA		Contact PM	
RX631	Please use RX63N as sample				Contact PM	
RX63T	R5F563T6EDFL	64kB	48pin LQFP	3V	Sampling now	
	R5F563T6EDFM	64KB	64pin LQFP	3V	Sampling now	
RX210	R5F5210BBDFB#V0	1024k	PLQP0144KB-A		Contact PM	R5F5210BBDFB#V0, R5F5210ABDFB#V0, R5F52108BDFB#V0, R5F52107BDFB#V0, R5F52107BDFB#V0
	R5F5210BBDFP#V0	1024k	PLQP0100KB-A		Contact PM	R5F5210BBDFP#V0, R5F5210ABDFP#V0, R5F52108BDFP#V0, R5F52107BDFP#V0, R5F52107BDFP#V0
	R5F52108ADFM#V0	512k	PLQP0064KB-A		mass production stock	R5F52108ADFM#V0, R5F52108ADFM#V0
	R5F52108ADLJ	512k	PTLG0100JA-A		Samples in stock	R5F52108ADLJ, R5F52107ADLJ
	R5F52108ADFN#V0	512k	PLQP0080KB-A		mass production stock	R5F52108ADFN#V0, R5F52107ADFN#V0
	R5F52108ADFP#V0	512k	PLQP0100KB-A		mass production stock	R5F52108ADFP#V0, R5F52107ADFP#V0
	R5F52106BDFL#V0	256K	PLQP0048KB-A		Samples in stock	R5F52106BDFL#V0, R5F52105BDFL#V0, R5F52104BDFL#V0, R5F52103BDFL#V0
	R5F52106BDFM#V0	256k	PLQP0064KB-A		Samples in stock	R5F52106BDFM#V0, R5F52105BDFM#V0, R5F52104BDFM#V0, R5F52103BDFM#V0
	R5F52106BDFP#V0	256k	PLQP0100KB-A		Samples in stock	R5F52106BDFP#V0, R5F52105BDFP#V0,
	R5F52106BDLJ	256k	PTLG0100JA-A		Contact PM	R5F52106BDLJ, R5F52105BDFLJ
RX220	R5F52206BDFL#V0	512k	PLQP0048KB-A		Contact PM	R5F52206BDFL#V0,R5F52205BDFL#V0,R5F52203BDFL#V0, R5F52201BDFL#V0
	R5F52206BDFM#V0	512k	PLQP0064KB-A		Contact PM	R5F52206BDFM#V0, R5F52205BDFM#V0, R5F52203BDFM#V0, R5F52201BDFM#V0
	R5F52206BDFP#V0	512k	PLQP0100KB-A		Samples in stock	R5F52206BDFP#V0, R5F52206BDFP#V0, R5F52206BDFP#V0
RX21A	R5F521A8BDFM#V0	512k	PLQP0064KB-A		Samples in stock	R5F521A8BDFM#V0, R5F521A7BDFM#V0, R5F521A6BDFM#V0
	R5F521A8BDFN#V0	512k	PLQP0080KB-A		Contact PM	R5F521A8BDFN#V0, R5F521A7BDFN#V0, R5F521A6BDFN#V0
	R5F521A8BDFP#V0	512k	PLQP0100KB-A		Samples in stock	R5F521A8BDFP#V0, R5F521A7BDFP#V0, R5F521A6BDFP#V0

This document shows the status of the "umbrella" device that we hold sample stock of in Europe for the RX200 and RX600 families. This snapshot was taken on the 23rd of August 2012. Please check with Product Marketing if you require samples we are not showing in stock.

RX200 Part Numbering System

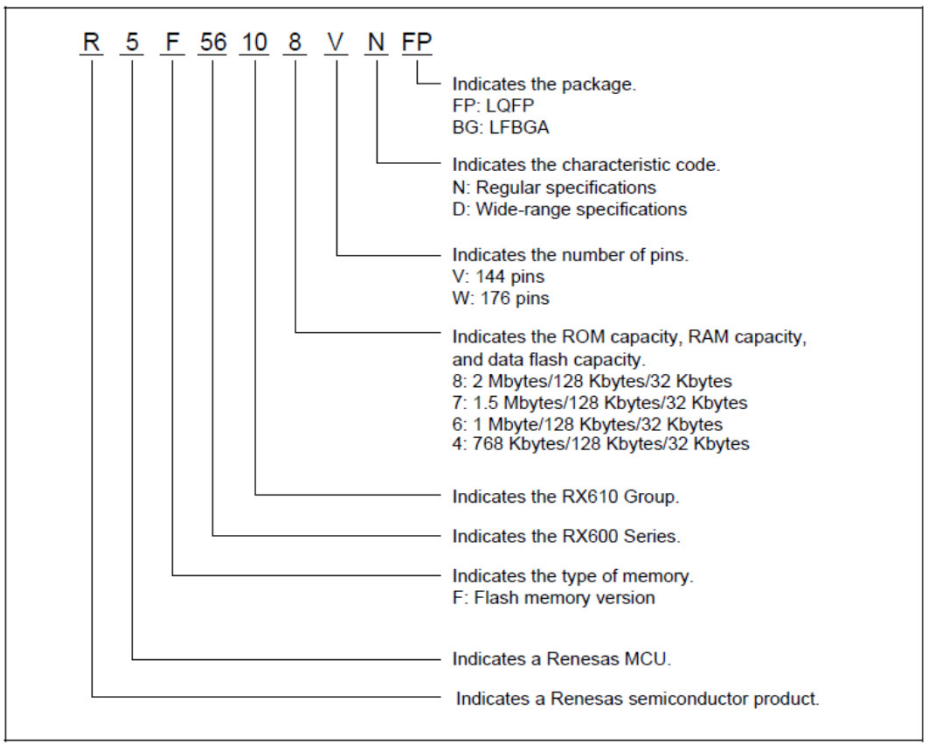

R5F52108ADFP#V0
Suffix
#U0 pkg material (LGA) / code Flash E/W 1K 10years
#V0 pkg material (LQFP) / code Flash E/W 1K 10years
#U1 pkg material (LGA) / code Flash E/W 1K 10years, 1K~10K 1year
#V1 pkg material (LQFP) / code Flash E/W 1K 10years, 1K~10K 1year
FB: LQFP-144 (0.50 mm pitch)
FP : LQFP-100 (0.50 mm pitch)
FN : LQFP-80 (0.50mm pitch)
FF : LQFP-80 (0.65mm pitch)
FM: LQFP-64 (0.50mm pitch)
FL: LQFP-48 (0.5 mm pitch)
FK : LQFP-64 (0.80mm pitch)
LK : TFLGA-145 (0.5 mm pitch)
LA: TFLGA-100 (0.5 mm pitch)
LJ : TFLGA-100 (0.65 mm pitch)
Operating temperature
D: -40°C to 85°C
G: -40°C to 105°C
Revision (A:, B: C: masks)
ROM capacity
1: 32 KB, 3: 64 KB, 4: 96 KB, 5: 128 KB, 6: 256 KB,
7: 384 KB, 8: 512 KB, A: 768k, B: 1 Mbyte
RX200 Group
10: RX210, 20: RX220, 1A: RX21A
RX200 Series
Memory type (F: flash-memory version)

RX Family Quick Reference (RX200 Selection Guide)

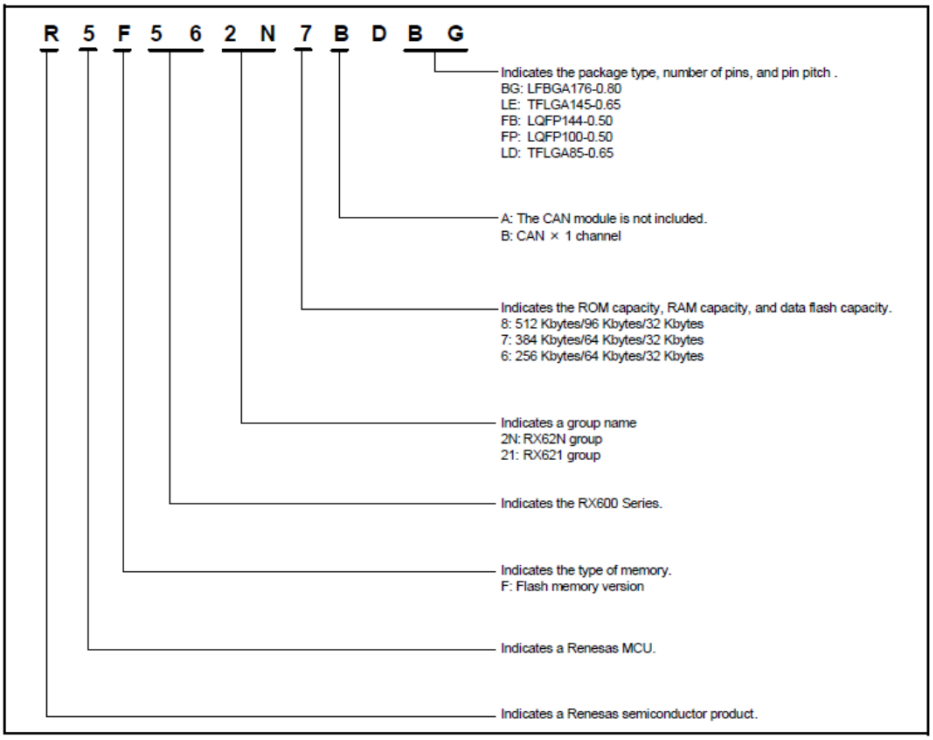
Sep-12

Device			Memory		Interfaces					Timers & other peripherals											Miscellaneous Information			
Core	Nickname	Part Number	Size [KB]	RAM [Byte]	I/O / Pins	SPI/ UARTs / I2C	CAN	USB	Ethernet	Timer Channels (8-bit/16-bit)	Special Timers	PWMs	Clock speed [MHz]	Int. Osc.	Subclock 32.768 kHz	TFT control	A/D / D/A-Converter	Analog features	DMA	Supply Voltage [V]	Packages	Qualification	Others features*	Starter Kit
RX200	RX210	R5F5210BBDLK	1M + 8k	96k	122 / 145 pins	13x USARTs (7 x I2C / SPI) 1 x I2C 1 x SPI	No	No	No	6 ch 16-bit MTU2 6 ch 16-bit TPU 4 ch 16-bit CMT 4 ch 8-bit TMR	MTU2 - High performance motor control timer Real Time Clock (RTC) Full clock / calandar with alarm and error correction 1 x WDT 1 x iWDT (with window function and independent oscillator	31	50 MHz	50 MHz 40 MHz 36.864 MHz 32 MHz 125 kHz	Yes	No	16 ch 12-bit ADC 2 ch 10-bit DAC	4 ch comparator Power on reset Circuit (POR) Low Voltage Decection Circuit (LVD) Temperature Sensor	4 ch DMAC DTC	1.62 - 5.5v	PTLG0145KA-A	Industrial (-40 -> + 85 degrees, -40 -> + 105 degrees)	DSP Function, Clock Accuracy Check (CAC) CRC Calculator (CRC) Data Operation Circuit (DOC) Event Link Controller (ELC) Multi-function Pin Controller (MPC)	R0K5052 10S000B E
		R5F52105BDLK	128k + 8k	20k																				
		R5F5210ABDLK	768k + 8k	64k																	PLQP0144KA-A			
		R5F52108BDLK	512k + 8k																					
		R5F52107BDLK	384k + 8k																					
		R5F52106BDLK	256k + 8k	32k																				
		R5F52105BDLK	128k + 8k	20k																				
		R5F5210BBDFB	1M + 8k	96k	122 / 144 pins																			
		R5F5210ABDFB	768k + 8k	64k																				
		R5F52108BDFB	512k + 8k																					
		R5F52107BDFB	384k + 8k																					
		R5F52106BDFB	256k + 8k	32k																				
		R5F52105BDFB	128k + 8k	20k																				
		R5F5210BBDLK	1M + 8k	96k	85 / 100 pins	7 x USARTs (7 x I2C / SPI) 1 x I2C 1 x SPI				6 ch 16-bit MTU2 4 ch 16-bit CMT 4 ch 8-bit TMR		16												
		R5F5210ABDLK	768k + 8k	64k																				
		R5F52108ADFP	512k + 8k																					
		R5F52107ADFP	384k + 8k																					
		R5F52106ADFP	256k + 8k	32k																				
		R5F52105ADFP	128k + 8k	20k																				
		R5F52108ADLJ	512k + 8k	64k																				
		R5F52107ADLJ	384k + 8k																					
		R5F52106ADLJ	256k + 8k	32k																				
		R5F52105ADLJ	128k + 8k	20k																				
		R5F52108ADFN	512k + 8k	64k	65 / 80 pins	7 x USARTs (7 x I2C / SPI) 1 x I2C 1 x SPI											14 ch 12-bit ADC 2 ch 10-bit DAC				PLQP0080KB-A			
		R5F52107ADFN	384k + 8k																					
		R5F52106ADFN	256k + 8k	32k																	PLQP0080JA-A			
		R5F52105ADFN	128k + 8k	20k																				
		R5F52108ADFF	512k + 8k	64k																	PLQP0064KB-A			
		R5F52107ADFF	384k + 8k																					
		R5F52106ADFF	256k + 8k	32k																				
		R5F52105ADFF	128k + 8k	20k																				
		R5F52108ADFM	512k + 8k	64k	49 / 64 pins	6 x USARTs (6 x I2C / SPI) 1 x I2C 1 x SPI											12 ch 12-bit ADC 2 ch 10-bit DAC				PLQP0064GA-A			
		R5F52107ADFM	384k + 8k																					
		R5F52106ADFM	256k + 8k	32k																				
		R5F52105ADFM	128k + 8k	20k																				
		R5F52108ADFK	512k + 8k	64k																				
		R5F52107ADFK	384k + 8k																					
		R5F52106ADFK	256k + 8k	32k																				
		R5F52105ADFK	128k + 8k	20k																				
		R5F52108CDFP	512k + 8k	64k	85 / 100 pins	7 x USARTs (7 x I2C / SPI) 1 x I2C 1 x SPI											16 ch 12-bit ADC 2 ch 10-bit DAC				PLQP0100KB-A			
		R5F52107CDFP	384k + 8k																					
		R5F52106CDFP	256k + 8k	32k																				
		R5F52105CDFP	128k + 8k	20k																				
		R5F52108CDLJ	512k + 8k	64k																	PTLG0100JA-A			
		R5F52107CDLJ	384k + 8k																					
		R5F52106CDLJ	256k + 8k	32k																				
		R5F52105CDLJ	128k + 8k	20k																				
		R5F52108CDFN	512k + 8k	64k	65 / 80 pins	7 x USARTs (7 x I2C / SPI) 1 x I2C 1 x SPI											14 ch 12-bit ADC 2 ch 10-bit DAC				PLQP0080KB-A			
		R5F52107CDFN	384k + 8k																					
		R5F52106CDFN	256k + 8k	32k																	PLQP0080JA-A			
		R5F52105CDFN	128k + 8k	20k																				
		R5F52108CDFF	512k + 8k	64k																	PLQP0064KB-A			
		R5F52107CDFF	384k + 8k																					
		R5F52106CDFF	256k + 8k	32k																				
		R5F52105CDFF	128k + 8k	20k																				
		R5F52108CDFM	512k + 8k	64k	49 / 64 pins	6 x USARTs (6 x I2C / SPI) 1 x I2C 1 x SPI											12 ch 12-bit ADC 2 ch 10-bit DAC				PLQP0064GA-A			
		R5F52107CDFM	384k + 8k																					
		R5F52106CDFM	256k + 8k	32k																				
		R5F52105BDFM	128k + 8k	20k																				
		R5F52108BDFK	512k + 8k	64k																				
		R5F52107BDFK	384k + 8k																					
		R5F52106BDFK	256k + 8k	32k																				
		R5F52105BDFK	128k + 8k	20k																				
		R5F52103BDFK	96k + 8k	16k																				
		R5F52101BDFK	64k + 8k	12k																				
		R5F52106BDFL	256k + 8k	32k	35 / 48 pins	5 x USARTs (5 x I2C / SPI) 1 x I2C 1 x SPI											8 ch 12-bit ADC				PLQP0048KB-A			
		R5F52105BDFL	128k + 8k	20k																				
		R5F52103BDFL	96k + 8k	16k																				
		R5F52101BDFL	64k + 8k	12k																				
	RX220	R5F52106BDFP	256k + 8k	16k	85 / 100 pins	5 x USARTs (5 x I2C / SPI) 1 x I2C 1 x SPI					MTU2 - High performance motor control timer Real Time Clock (RTC) Full clock / calandar with alarm and error correction 1 x WDT 1 x iWDT (with window function and independent oscillator		32 MHz		No		16 ch 12-bit ADC	2 ch comparator Power on reset Circuit (POR) Low Voltage Decection Circuit (LVD)		1.8 - 3.6v	PLQP0100KB-A	Industrial (-40 -> + 85 degrees)	DSP Function, Clock Accuracy Check (CAC) CRC (CRC) Data Operation Circuit (DOC) Event Link Controller (ELC) Multi-function Pin	
		R5F52105BDFP	128k + 8k	8k																				
		R5F52103BDFP	64k + 8k	8k																				
		R5F52106BDFM	256k + 8k	16k	49 / 64 pins												12 ch 12-bit ADC				PLQP0064KB-A			
		R5F52105BDFM	128k + 8k	8k																				
		R5F52103BDFM	64k + 8k	8k																				
		R5F52101BDFM	32k + 8k	4k																				
		R5F52106BDFK	256k + 8k	16k																	PLQP0064GA-A			
		R5F52105BDFK	128k + 8k	8k																				
		R5F52103BDFK	64k + 8k	8k																				
		R5F52101BDFK	32k + 8k	4k																				
		R5F52106BDFL	256k + 8k	16k	35 / 48 pins	4 x USARTs (4 x I2C / SPI) 1 x I2C 1 x SPI					MTU2 1 x iWDT				No		8 ch 12-bit ADC				PLQP0048KB-A			
		R5F52105BDFL	128k + 8k	8k																				
		R5F52103BDFL	64k + 8k	8k																				
		R5F52101BDFL	32k + 8k	4k																				
	RX21A	R5F521A8BDFP	512k + 8k	64k	65 / 100 pins	5 x USARTs (5 x I2C / SPI) 2 x I2C 2 x SPI					MTU2 RTC Full clock / calandar with alarm and error correction 1 x WDT 1 x iWDT (with window function and independent oscillator		50 MHz		Yes		7 modules 24-bit ADC, 7 ch 10-bit ADC, 2 ch 10-bit DAC	4 ch comparator Power on reset Circuit (POR) Low Voltage Decection Circuit (LVD) Temperature Sensor		1.62 - 3.6v	PLQP0100KB-A		DSP Function, CAC, CRC , DOC, ELC, MPC.	TBC
		R5F521A7BDFP	384k + 8k																					
		R5F521A6BDFP	256k + 8k	32k																				
		R5F521A8BDFN	512k + 8k	64k	51 / 80 pins												4 modules 24-bit ADC, 7 ch 10-bit ADC, 2 ch 10-bit DAC				PLQP0080KB-A			
		R5F521A7BDFN	384k + 8k																					
		R5F521A6BDFN	256k + 8k	32k																				
		R5F521A8BDFM	512k + 8k	64k	38 / 64 pins	5 x USARTs (5 x I2C / SPI) 1 x I2C 2 x SPI											3 modules 24-bit ADC, 3 ch 10-bit ADC,				PLQP0064KB-A			
		R5F521A7BDFM	384k + 8k																					
		R5F521A6BDFM	256k + 8k	32k																				

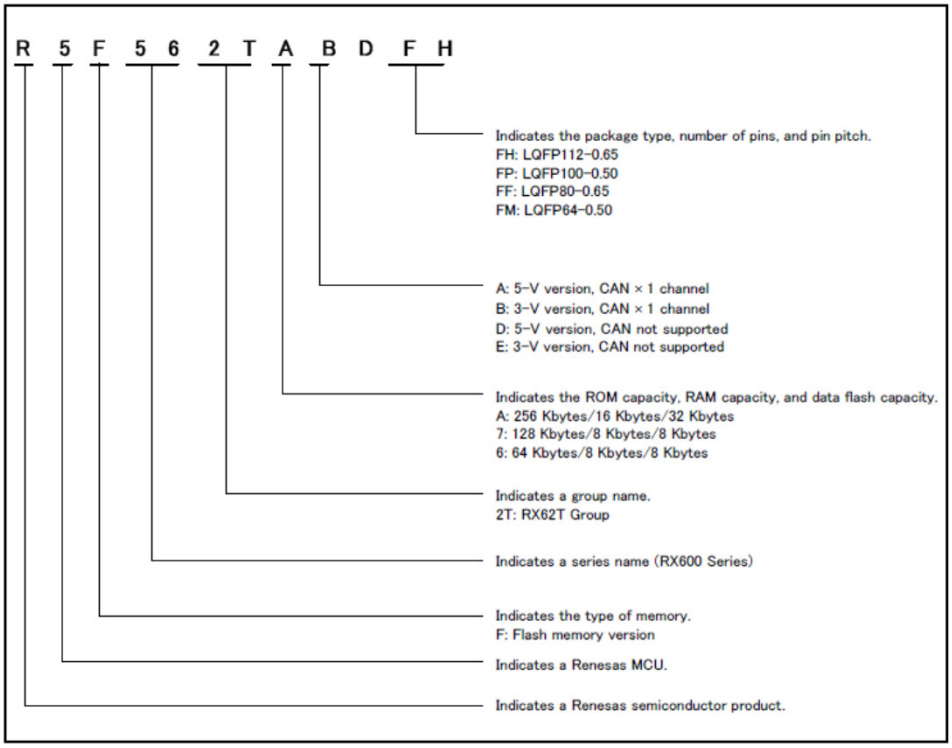
RX610


R 5 F 56 10 8 V N FP
Indicates the package.
FP: LQFP
BG: LFBGA
Indicates the characteristic code.
N: Regular specifications
D: Wide-range specifications
Indicates the number of pins.
V: 144 pins
W: 176 pins
Indicates the ROM capacity, RAM capacity, and data flash capacity.
8: 2 Mbytes/128 Kbytes/32 Kbytes
7: 1.5 Mbytes/128 Kbytes/32 Kbytes
6: 1 Mbyte/128 Kbytes/32 Kbytes
4: 768 Kbytes/128 Kbytes/32 Kbytes
Indicates the RX610 Group.
Indicates the RX600 Series.
Indicates the type of memory.
F: Flash memory version
Indicates a Renesas MCU.
Indicates a Renesas semiconductor product.

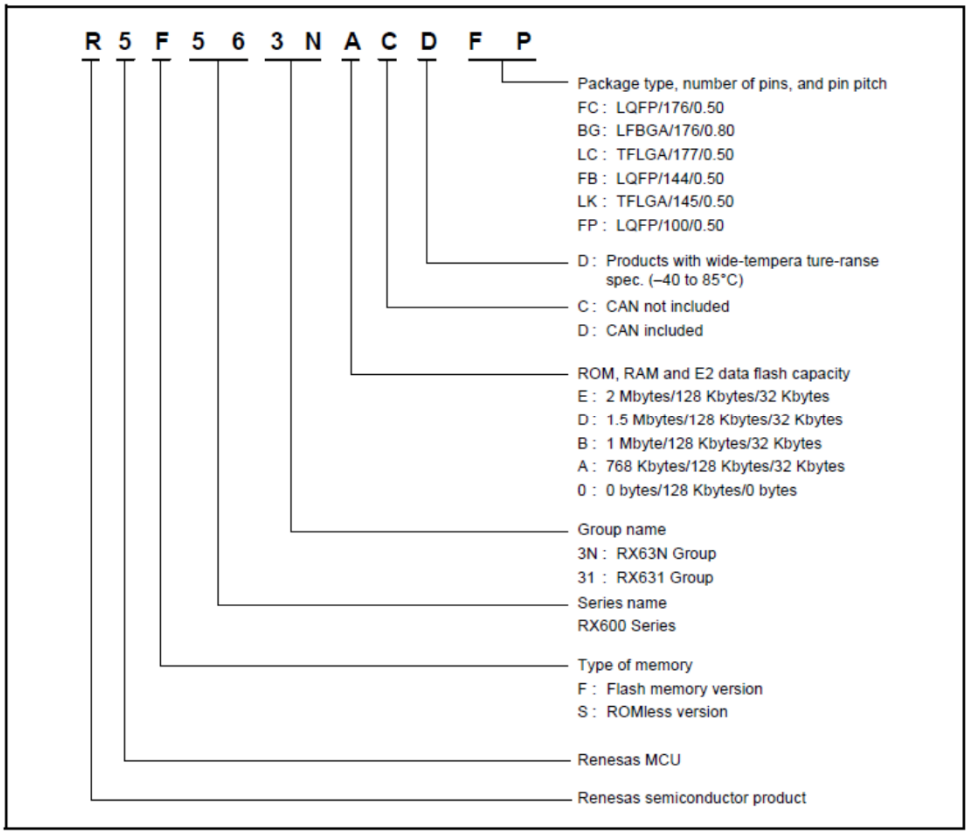
RX621 and RX62N


R 5 F 5 6 2 N 7 B D B G
Indicates the package type, number of pins, and pin pitch .
BG: LFBGA176-0.80
LE: TFLGA145-0.65
FB: LQFP144-0.50
FP: LQFP100-0.50
LD: TFLGA85-0.65
A: The CAN module is not included.
B: CAN × 1 channel
Indicates the ROM capacity, RAM capacity, and data flash capacity.
8: 512 Kbytes/96 Kbytes/32 Kbytes
7: 384 Kbytes/64 Kbytes/32 Kbytes
6: 256 Kbytes/64 Kbytes/32 Kbytes
Indicates a group name
2N: RX62N group
21: RX621 group
Indicates the RX600 Series.
Indicates the type of memory.
F: Flash memory version
Indicates a Renesas MCU.
Indicates a Renesas semiconductor product.

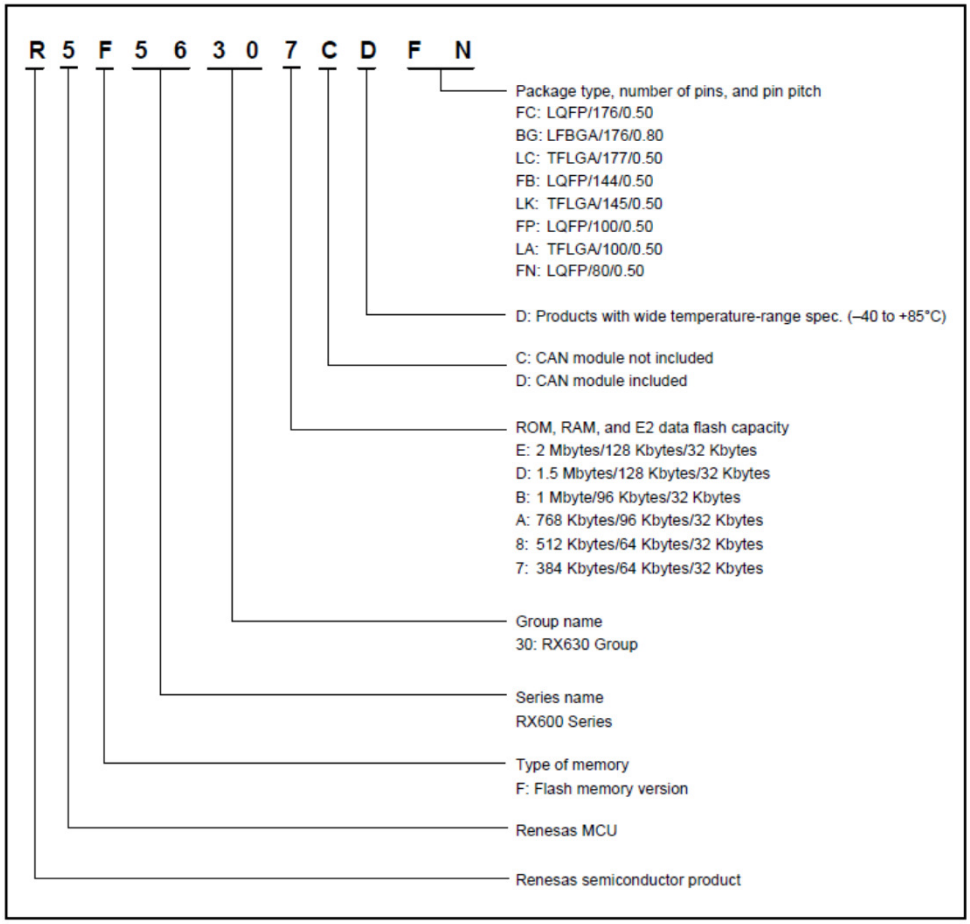
RX62T


R 5 F 5 6 2 T A B D F H
Indicates the package type, number of pins, and pin pitch.
FH: LQFP112-0.65
FP: LQFP100-0.50
FF: LQFP80-0.65
FM: LQFP64-0.50
A: 5-V version, CAN × 1 channel
B: 3-V version, CAN × 1 channel
D: 5-V version, CAN not supported
E: 3-V version, CAN not supported
Indicates the ROM capacity, RAM capacity, and data flash capacity.
A: 256 Kbytes/16 Kbytes/32 Kbytes
7: 128 Kbytes/8 Kbytes/8 Kbytes
6: 64 Kbytes/8 Kbytes/8 Kbytes
Indicates a group name.
2T: RX62T Group
Indicates a series name (RX600 Series)
Indicates the type of memory.
F: Flash memory version
Indicates a Renesas MCU.
Indicates a Renesas semiconductor product.

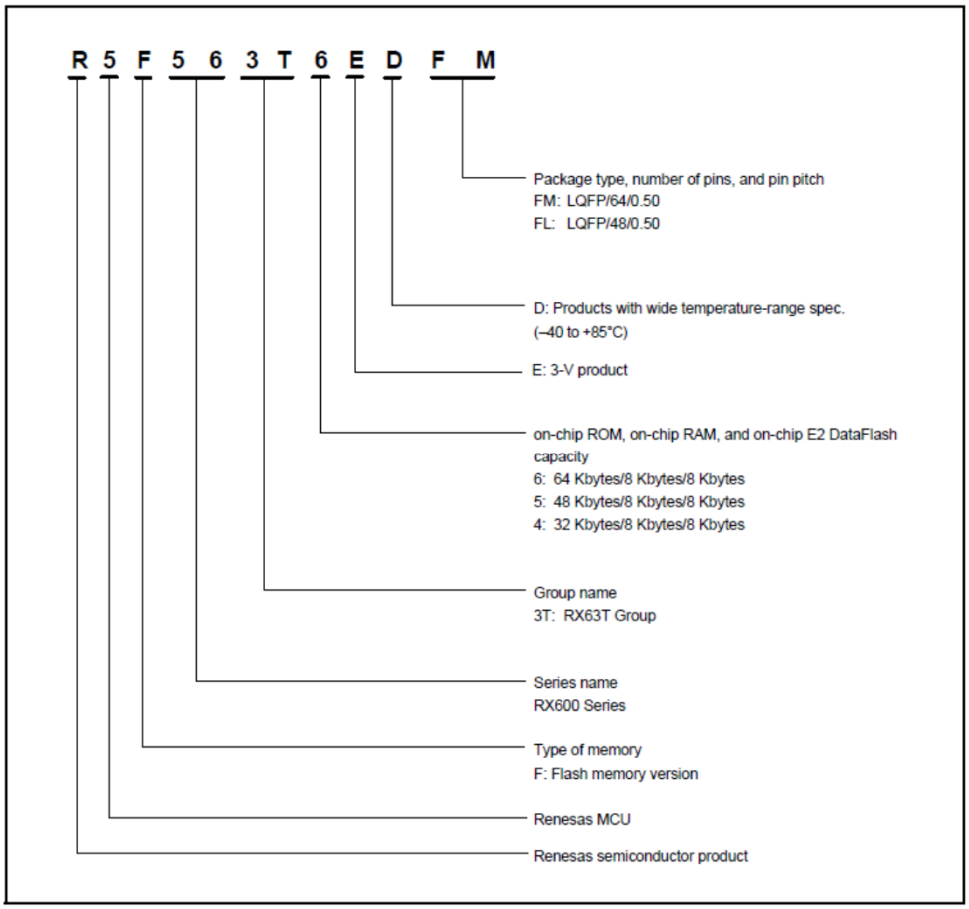
RX631 and RX63N


R 5 F 5 6 3 N A C D F P
Package type, number of pins, and pin pitch
FC : LQFP/176/0.50
BG : LFBGA/176/0.80
LC : TFLGA/177/0.50
FB : LQFP/144/0.50
LK : TFLGA/145/0.50
FP : LQFP/100/0.50
D : Products with wide-tempera ture-ranse spec. (–40 to 85°C)
C : CAN not included
D : CAN included
ROM, RAM and E2 data flash capacity
E : 2 Mbytes/128 Kbytes/32 Kbytes
D : 1.5 Mbytes/128 Kbytes/32 Kbytes
B : 1 Mbyte/128 Kbytes/32 Kbytes
A : 768 Kbytes/128 Kbytes/32 Kbytes
0 : 0 bytes/128 Kbytes/0 bytes
Group name
3N : RX63N Group
31 : RX631 Group
Series name
RX600 Series
Type of memory
F : Flash memory version
S : ROMless version
Renesas MCU
Renesas semiconductor product

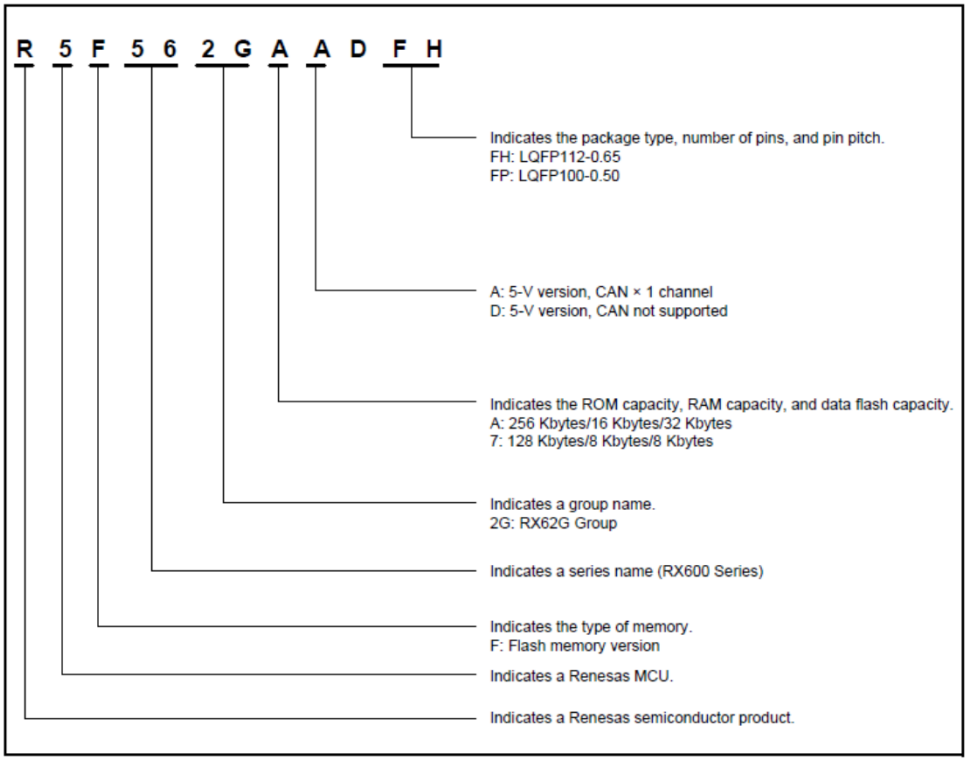
RX630


R 5 F 5 6 3 0 7 C D F N
Package type, number of pins, and pin pitch
FC: LQFP/176/0.50
BG: LFBGA/176/0.80
LC: TFLGA/177/0.50
FB: LQFP/144/0.50
LK: TFLGA/145/0.50
FP: LQFP/100/0.50
LA: TFLGA/100/0.50
FN: LQFP/80/0.50
D: Products with wide temperature-range spec. (–40 to +85°C)
C: CAN module not included
D: CAN module included
ROM, RAM, and E2 data flash capacity
E: 2 Mbytes/128 Kbytes/32 Kbytes
D: 1.5 Mbytes/128 Kbytes/32 Kbytes
B: 1 Mbyte/96 Kbytes/32 Kbytes
A: 768 Kbytes/96 Kbytes/32 Kbytes
8: 512 Kbytes/64 Kbytes/32 Kbytes
7: 384 Kbytes/64 Kbytes/32 Kbytes
Group name
30: RX630 Group
Series name
RX600 Series
Type of memory
F: Flash memory version
Renesas MCU
Renesas semiconductor product

RX63T


R 5 F 5 6 3 T 6 E D F M
Package type, number of pins, and pin pitch
FM: LQFP/64/0.50
FL: LQFP/48/0.50
D: Products with wide temperature-range spec.
(–40 to +85°C)
E: 3-V product
on-chip ROM, on-chip RAM, and on-chip E2 DataFlash capacity
6: 64 Kbytes/8 Kbytes/8 Kbytes
5: 48 Kbytes/8 Kbytes/8 Kbytes
4: 32 Kbytes/8 Kbytes/8 Kbytes
Group name
3T: RX63T Group
Series name
RX600 Series
Type of memory
F: Flash memory version
Renesas MCU
Renesas semiconductor product

RX62G


R 5 F 5 6 2 G A A D F H
Indicates the package type, number of pins, and pin pitch.
FH: LQFP112-0.65
FP: LQFP100-0.50
A: 5-V version, CAN × 1 channel
D: 5-V version, CAN not supported
Indicates the ROM capacity, RAM capacity, and data flash capacity.
A: 256 Kbytes/16 Kbytes/32 Kbytes
7: 128 Kbytes/8 Kbytes/8 Kbytes
Indicates a group name.
2G: RX62G Group
Indicates a series name (RX600 Series)
Indicates the type of memory.
F: Flash memory version
Indicates a Renesas MCU.
Indicates a Renesas semiconductor product.

RX Family Quick Reference (RX600 Selection Guide)

Sep-12

Device		Memory		Operation		Interfaces								Timers						Clock			Parallel I/F		Analog						Miscellaneous Information			
Group	Part Number	Flash [Byte]	RAM [Byte]	Max. Clock speed [MHz]	Supply Voltage [V]	I/O	SPI	SCI	I2C	LIN	CAN	USB (Host/Device/OTG)	USB Device	Ethernet	Timer 8bit	Timer 16bit	Motor	IWDT	WDT	RTC	LOCO	HOCO	32.768 kHz	TFT LCD	Ext. Data Bus	10bit ADC	12bit ADC	10bit DAC	ProgOpAmp	POR & LVD	DMA	DTC	Packages	Qualification	Others features
RX610	R5F56108WDB	2048k + 32k	128k	100	3.0-3.6V	140	7	7	2	-	-	-	-	-	4	22	-	-	1		-	-	-	-	Yes	4x 4ch	-	2ch	-	-	4	yes	BGA 176pin 13x13mm 0.8mm pitch	Industrial -40℃ to 85℃	FPU; DSP RMPA; Barrel Shifter; Programmable pattern generator (PPG)
	R5F56107WDB	1536k + 32k																																
	R5F56106WDB	1024k + 32k																																
	R5F56104WDB	768k + 32k																																
	R5F56108VDFP	2048k + 32k				117																										LQFP 144pin 20x20mm 0.5mm pitch		
	R5F56107VDFP	1536k + 32k																																
	R5F56106VDFP	1024k + 32k																																
	R5F56104VDFP	768k + 32k																																
RX621	R5F56218BDBG	512k + 32k	96k	100	2.7-3.6V	128	2	6	2	-	1	2	-	-	4	16	MTU2	1	1	1	1	-	yes	yes	SDRAM	2x 4ch or	or 8ch	2ch	-	yes	4	yes	BGA 176pin 13x13mm	Industrial -40℃ to 85℃	FPU; DSP RMPA; Barrel Shifter; SDRAM Interface Programmable pattern Generator (PPG), RTC, CRC unit
	R5F56217BDBG	384k + 32k	64k																															
	R5F56216BDBG	256k + 32k																																
	R5F56218BDLE	512k + 32k	96k			105						1																				LGA 145pin 9x9mm		
	R5F56217BDLE	384k + 32k	64k																															
	R5F56216BDLE	256k + 32k																																
	R5F56218BDFB	512k + 32k	96k			105																										LQFP 144pin 20x20mm		
	R5F56217BDFB	384k + 32k	64k																															
	R5F56216BDFB	256k + 32k																																
	R5F56218BDFP	512k + 32k	96k			74	2	6	1	-														-	Yes							LQFP 100pin 14x14mm		FPU; DSP RMPA; Barrel Shifter; Programmable pattern Generator (PPG), RTC, CRC unit
	R5F56217BDFP	384k + 32k	64k																															
	R5F56216BDFP	256k + 32k																																
	R5F56218BDLD	512k + 32k	96k			60																										LGA 85pin 7x7mm		
	R5F56217BDLD	384k + 32k	64k																															
	R5F56216BDLD	256k + 32k																																
RX62N	R5F562N8BDBG	512k + 32k	96k	100	2.7-3.6V	128	2	6	2	-	1	2	-	Y	4	16	MTU2	1	1	1	1	-	yes	yes	SDRAM	2x 4ch or	or 8ch	2ch	-	yes	1	yes	BGA 176pin 13x13mm 0.8mm pitch	Industrial -40℃ to 85℃	FPU; DSP RMPA; Barrel Shifter; SDRAM Interface Programmable pattern Generator (PPG), RTC, Ethernet DMA, CRC unit
	R5F562N8ADBG	512k + 32k									-																							
	R5F562N7BDBG	384k + 32k	64k								1																							
	R5F562N7ADBG	384k + 32k									-																							
	R5F562N8BDLE	512k + 32k	96k			105					1																					LGA 145pin 9x9mm 0.65mm		
	R5F562N8ADLE	512k + 32k									-																							
	R5F562N7BDLE	384k + 32k	64k								1																							
	R5F562N7ADLE	384k + 32k									-																							
	R5F562N8BDFB	512k + 32k	96k			105					1																					LQFP 144pin 20x20mm 0.5mm pitch		
	R5F562N8ADFB	512k + 32k									-																							
	R5F562N7BDFB	384k + 32k	64k								1																							
	R5F562N7ADFB	384k + 32k									-																							
	R5F562N8BDFP	512k + 32k	96k			74	2	6	1	-	1													-	Yes							LQFP 100pin 14x14mm 0.5mm pitch		FPU; DSP RMPA; Barrel Shifter; Programmable pattern Generator (PPG), RTC,
	R5F562N8ADFP	512k + 32k									-																							
	R5F562N7BDFP	384k + 32k	64k								1																							
	R5F562N7ADFP	384k + 32k									-																							
RX62T	R5F562TABDFF	256k + 32k	16k	100	2.7-3.6V	76	1	3	1	1	1	-	-	-	-	14	MTU3 and GPT	1	1	-	1	-	-	-	-	12ch	2x 4ch	-	6	yes	-	yes	LQFP 100pin 14x14mm 0.5mm pitch	Industrial -40℃ to 85℃	FPU; DSP RMPA; Barrel Shifter; External input (POE) Windows comparator; Clock stop detection Clock monitoring; ADC diagnostic, CRC unit
	R5F562TAADFF	256k + 32k			4.0-5.5V						1																							
	R5F562TAEDFF	256k + 32k			2.7-3.6V						-																							
	R5F562TADDFF	256k + 32k			4.0-5.5V						-																							
	R5F562T7BDFF	128k + 8k	8k		2.7-3.6V						1																							
	R5F562T7ADFF	128k + 8k			4.0-5.5V						1																							
	R5F562T7EDFF	128k + 8k			2.7-3.6V						-																							
	R5F562T7DDFF	128k + 8k			4.0-5.5V						-																							
	R5F562T6BDFF	64k + 8 k			2.7-3.6V						1																							
	R5F562T6ADFF	64k + 8 k			4.0-5.5V						1																							
	R5F562T6EDFF	64k + 8 k			2.7-3.6V						-																							
	R5F562T6DDFF	64k + 8 k			4.0-5.5V						-																							
	R5F562TABDFM	256k + 32k	16k		2.7-3.6V	57					1				-	10										4ch	2x 4ch						LQFP 80pin 14x14mm 0.65mm pitch		
	R5F562TAADFM	256k + 32k			4.0-5.5V						1																							
	R5F562TAEDFM	256k + 32k			2.7-3.6V						-																							
	R5F562TADDFM	256k + 32k			4.0-5.5V						-																							
	R5F562T7BDFM	128k + 8k	8k		2.7-3.6V	46					1				-	10										-	2x 4ch						LQFP 64pin 10x10mm 0.5mm pitch		
	R5F562T7ADFM	128k + 8k			4.0-5.5V						1																							
	R5F562T7EDFM	128k + 8k			2.7-3.6V						-																							
	R5F562T7DDFM	128k + 8k			4.0-5.5V						-																							
	R5F562T6BDFM	64k + 8 k			2.7-3.6V						1																							
	R5F562T6ADFM	64k + 8 k			4.0-5.5V						1																							
	R5F562T6EDFM	64k + 8 k			2.7-3.6V						-																							
	R5F562T6DDFM	64k + 8 k			4.0-5.5V						-																							
RX62G	R5F562GAADFH	256k + 32k	16k	100	4.0-5.5V	82	1	3	1	1	1	-	-	-	-	14	MTU3 and GPT	1	1	-	1	-	-	-	-	12ch	2x 4ch	-	6	yes	-	yes	LQFP 112pin 20x20mm 0.65mm	Industrial -40℃ to 85℃	FPU; DSP RMPA; Barrel Shifter; External input (POE) Windows comparator; Clock stop detection Clock monitoring; ADC diagnostic, CRC unit High Resolution timer
	R5F562GADDFH	256k + 32k									-																							
	R5F562G7ADFH	128k + 8k	8k								1																							
	R5F562G7DDFH	128k + 8k									-																							
	R5F562GAADFP	256k + 32k	16k			76					1																					LQFP 100pin 14x14mm 0.5mm pitch		
	R5F562GADDFP	256k + 32k									-																							
	R5F562G7ADFP	128k + 8k	8k								1																							
	R5F562G7DDFP	128k + 8k									-																							
RX630	R5F5630EDDFC	2048k + 32k	128k	100	2.7-3.6V	133	3	13	4	-	3				4	22	MTU2	1	1	1 Vbat anti tamper	1	1	yes	-	Yes	8ch	21ch	2ch	-	yes	4	yes	LQFP 176pin 24x24mm 0.5mm	Industrial -40℃ to 85℃	FPU; DSP RMPA; Barrel Shifter; Programmable pattern Generator (PPG), RTC with Vbat, CRC unit, Temperature sensor
	R5F5630DDDFC	1536k + 32k									2																							
	R5F5630BDDFC	1024k + 32k	96k								1																							
	R5F5630ADDFC	768k + 32k									-																							
	R5F56308DDFC	512k + 32k	64k								1																							
	R5F5630EDDBG	2048k + 32k	128k			133					3																					BGA 176pin 13x13mm 0.8mm		
	R5F5630DDDBG	1536k + 32k									2																							
	R5F5630BDDBG	1024k + 32k	96k								1																							
	R5F5630ADDBG	768k + 32k									-																							
	R5F56308DDBG	512k + 32k	64k								1																							
	R5F56307DDBG	384k + 32k									-																							
	R5F5630EDDLK	2048k + 32k	128k			111					3																					LGA 145pin 7x7mm 0.5mm		
	R5F5630DDDLK	1536k + 32k									2																							
	R5F5630BDDLK	1024k + 32k	96k								1																							
	R5F5630ADDLK	768k + 32k									-																							
	R5F56308DDLK	512k + 32k	64k								1																							
	R5F56307DDLK	384k + 32k									-																							
	R5F5630EDDFB	2048k + 32k	128k			111					3																					LQFP 144pin 20x20mm 0.5mm		
	R5F5630DDDFB	1536k + 32k									2																							
	R5F5630BDDFB	1024k + 32k	96k								1																							
	R5F5630ADDFB	768k + 32k									-																							
	R5F56308DDFB	512k + 32k	64k								1																							
	R5F56307DDFB	384k + 32k									-																							
	R5F5630EDDFP	2048k + 32k	128k			78	2	9	2	-	2				4	16											14ch	1ch					LQFP 100pin 14x14mm 0.5mm		
	R5F5630DDDFP	1536k + 32k									1																							
	R5F5630BDDFP	1024k + 32k	96k																															
	R5F5630ADDFP	768k + 32k																																
	R5F56308DDFP	512k + 32k	64k																															
	R5F56307DDFP	384k + 32k																																
	R5F56306DDFP	256k + 32k	48k																															
	R5F56308DDFN	512k + 32k	64k			58	2	6	2	-	1				4	16									-	4ch	10ch								
	R5F56307DDFN	384k + 32k																																
RX631	R5F5631EDDFC	2048k + 32k	128k	100	2.7-3.6V	133	3	13	4	-	3	2	-	-	4	22	MTU2	1	1	1 Vbat anti tamper	1	1	yes	yes	SDRAM	8ch	21ch	2ch	-	yes	4	yes	LQFP 177pin 24x24mm 0.5mm	Industrial -40℃ to 85℃	FPU; DSP RMPA; Barrel Shifter; SDRAM Interface Programmable pattern Generator (PPG), RTC with Vbat (177-64pin), CRC unit, Temperature sensor
	R5F5631EDDBG	2048k + 32k																														BGA 176pin 13x13mm 0.8mm		
	R5F5631DDDBG	1536k + 32k	128k			133																												
	R5F5631BDDBG	1024k + 32k																																
	R5F5631ADDBG	768k + 32k																																
	R5F5631EDDLK	2048k + 32k	128k			111																										LGA 145pin 7x7mm 0.5mm		
	R5F5631DDDLK	1536k + 32k																																
	R5F5631BDDLK	1024k + 32k	96k																															
	R5F5631ADDLK	768k + 32k																																
	R5F5631EDDFB	2048k + 32k	128k			111																										LQFP 144pin 20x20mm 0.5mm		
	R5F5631DDDFB	1536k + 32k																																
	R5F5631BDDFB	1024k + 32k	96k																															
	R5F5631ADDFB	768k + 32k																																
	R5F5631EDDFP	2048k + 32k	128k			78	2	9	2	-	2	1	-	-	4	16								yes	Yes		14ch	1ch					LQFP 100pin 14x14mm 0.5mm		
	R5F5631DDDFP	1536k + 32k																																
	R5F5631BDDFP	1024k + 32k	96k																															
	R5F5631ADDFP	768k + 32k																																
	R5F5631PDDFM	512k + 32k	64k			42	2	6	1	-	1				4	16								-	-	-	12ch	1ch					LQFP 64pin 10x10mm		
	R5F5631MDDFM	256k + 32k																																
	R5F5631PDDFL	512k + 32k	64k			30	2	6	1	-	1																8ch	-					LQFP 48pin 7x7mm		
	R5F5631NDDFL	384k + 32k																																
	R5F5631MDDFL	256k + 32k																																
RX63N	R5F563NEDDFC	2048k + 32k	128k	100	2.7-3.6V	133	3	13	4	-	3	2	-	1	4	22	MTU2	1	1	1 Vbat anti tamper	1	1	yes	yes	SDRAM	8ch	21ch	2ch	-	yes	4	yes	LQFP 176pin 24x24mm 0.5mm	Industrial -40℃ to 85℃	FPU; DSP RMPA; Barrel Shifter; SDRAM Interface Programmable pattern Generator (PPG), RTC with Vbat, Ethernet DMA, CRC unit, Temperature sensor
	R5F563NDDDFC	1536k + 32k																																
	R5F563NBDDFC	1024k + 32k	96k																															
	R5F563NADDFC	768k + 32k																																
	R5F563NEDDBG	2048k + 32k	128k			133																										BGA 176pin 13x13mm 0.8mm		
	R5F563NDDDBG	1536k + 32k																																
	R5F563NBDDBG	1024k + 32k	96k																															
	R5F563NADDBG	768k + 32k																																
	R5F563NEDDLK	2048k + 32k	128k			111																										LGA 145pin 7x7mm 0.5mm		
	R5F563NDDDLK	1536k + 32k																																
	R5F563NBDDLK	1024k + 32k	96k																															
	R5F563NADDLK	768k + 32k																																
	R5F563NEDDFB	2048k + 32k	128k			111																										LQFP 144pin 20x20mm 0.5mm		
	R5F563NDDDFB	1536k + 32k																																
	R5F563NBDDFB	1024k + 32k	96k																															
	R5F563NADDFB	768k + 32k																																
	R5F563NEDDFP	2048k + 32k	128k			78	2	9	2	-	2	1	-		4	16									Yes		14ch						LQFP 100pin 14x14mm 0.5mm		
	R5F563NDDDFP	1536k + 32k																																
	R5F563NBDDFP	1024k + 32k	96k																															
	R5F563NADDFP	768k + 32k																																
RX63T	R5F563T6EDFM	64k + 8k	8k	100	2.7-3.6V	39	1	3	1	-	-	-	-	-	-	14	MTU3 and GPT	1	1	-	1	-	-	-	-	-	8ch	-	-	yes	4	yes	LQFP 64pin 10x10mm	Industrial -40℃ to 85℃	FPU; DSP RMPA; Barrel Shifter; External input (POE) Windows comparator; Clock stop detection Clock monitoring; ADC
	R5F563T5EDFM	48k + 8k																									6ch						LQFP 48pin 7x7mm		
	R5F563T4EDFM	32k + 8k				25																												

RX610 Group Quick Reference

Sep-12

Device	Flash (bytes)	Data Flash (bytes)	SRAM (bytes)	Operating Temperature	Operating Voltage/ Maximum Clock Rate	Package Code	Renesas Part Number	Renesas Order Code	Status
RX610	2048k	32k	128k	Regular	Vcc=3.0 to 3.6V	PLQP0144KA-A	R5F56108VNFP	R5F56108VNFP#V0	MP
				(-20 -> +85 degrees C)	f = 100MHz	PLBG0176GA-A	R5F56108WNBG	R5F56108WNBG#U0	
				D version		PLQP0144KA-A	R5F56108VDFP	R5F56108VDFP#V0	
				(-40 -> +85 degrees C)		PLBG0176GA-A	R5F56108WDBG	R5F56108WDBG#U0	
	1536k			Regular		PLQP0144KA-A	R5F56107VNFP	R5F56107VNFP#V0	
				(-20 -> +85 degrees C)		PLBG0176GA-A	R5F56107WNBG	R5F56107WNBG#U0	
				D version		PLQP0144KA-A	R5F56107VDFP	R5F56107VDFP#V0	
				(-40 -> +85 degrees C)		PLBG0176GA-A	R5F56107WDBG	R5F56107WDBG#U0	
	1024k			Regular		PLQP0144KA-A	R5F56106VNFP	R5F56106VNFP#V0	
				(-20 -> +85 degrees C)		PLBG0176GA-A	R5F56106WNBG	R5F56106WNBG#U0	
				D version		PLQP0144KA-A	R5F56106VDFP	R5F56106VDFP#V0	
				(-40 -> +85 degrees C)		PLBG0176GA-A	R5F56106WDBG	R5F56106WDBG#U0	
	768k			Regular		PLQP0144KA-A	R5F56104VNFP	R5F56104VNFP#V0	
				(-20 -> +85 degrees C)		PLBG0176GA-A	R5F56104WNBG	R5F56104WNBG#U0	
				D version		PLQP0144KA-A	R5F56104VDFP	R5F56104VDFP#V0	
				(-40 -> +85 degrees C)		PLBG0176GA-A	R5F56104WDBG	R5F56104WDBG#U0	

RX62N / RX621 Group Quick Reference

Sep-12

Device	Flash (bytes)	Data Flash (bytes)	SRAM (bytes)	Operating Temperature	Operating Voltage/ Maximum Clock Rate	Ethernet	CAN	USB (OTG, Host , Function	Package Code	Renesas Part Number	Renesas Order Code	Status
RX621	512k	32k	96k	D version	Vcc=2.7 to 3.6V	-	1	2	PLBG0176GA-A	R5F56218BDBG	R5F56218BDBG#U0	MP
				(-40 -> +85 degrees C)	f = 100MHz			1	PTLG0145JB-A	R5F56218BDLE	R5F56218BDLE#U0	
									PLQP0144KA-A	R5F56218BDFB	R5F56218BDFB#V0	
									PLQP0100KB-A	R5F56218BDFP	R5F56218BDFP#V0	
									PTLG0085JA-A	R5F56218BDLD	R5F56218BDLD#U0	
	384k	32k	64k			-	1	2	PLBG0176GA-A	R5F56217BDBG	R5F56217BDBG#U0	
								1	PTLG0145JB-A	R5F56217BDLE	R5F56217BDLE#U0	
									PLQP0144KA-A	R5F56217BDFB	R5F56217BDFB#V0	
									PLQP0100KB-A	R5F56217BDFP	R5F56217BDFP#V0	
									PTLG0085JA-A	R5F56217BDLD	R5F56217BDLD#U0	
	256k	32k	64k			-	1	2	PLBG0176GA-A	R5F56216BDBG	R5F56216BDBG#U0	
								1	PTLG0145JB-A	R5F56216BDLE	R5F56216BDLE#U0	
									PLQP0144KA-A	R5F56216BDFB	R5F56216BDFB#V0	
									PLQP0100KB-A	R5F56216BDFP	R5F56216BDFP#V0	
									PTLG0085JA-A	R5F56216BDLD	R5F56216BDLD#U0	
RX62N	512k	32k	96k	D version	Vcc=2.7 to 3.6V	Yes	1	2	PLBG0176GA-A	R5F562N8BDBG	R5F562N8BDBG#U0	MP
				(-40 -> +85 degrees C)	f = 100MHz		0			R5F562N8ADBG	R5F562N8ADBG#U0	
							1	1	PTLG0145JB-A	R5F562N8BDLE	R5F562N8BDLE#U0	
							0			R5F562N8ADLE	R5F562N8ADLE#U0	
							1		PLQP0144KA-A	R5F562N8BDFB	R5F562N8BDFB#V0	
							0			R5F562N8ADFB	R5F562N8ADFB#V0	
							1		PLQP0100KB-A	R5F562N8BDFP	R5F562N8BDFP#V0	
							0			R5F562N8ADFP	R5F562N8ADFP#V0	
	384k	32k	64k				1	2	PLBG0176GA-A	R5F562N7BDBG	R5F562N7BDBG#U0	
							0			R5F562N7ADBG	R5F562N7ADBG#U0	
							1	1	PTLG0145JB-A	R5F562N7BDLE	R5F562N7BDLE#U0	
							0			R5F562N7ADLE	R5F562N7ADLE#U0	
							1		PLQP0144KA-A	R5F562N7BDFB	R5F562N7BDFB#V0	
							0			R5F562N7ADFB	R5F562N7ADFB#V0	
							1		PLQP0100KB-A	R5F562N7BDFP	R5F562N7BDFP#V0	
							0			R5F562N7ADFP	R5F562N7ADFP#V0	

RX62T Group Quick Reference

Sep-12

Device	Flash (bytes)	Data Flash (bytes)	SRAM (bytes)	Operating Temperature	Operating Voltage/ Maximum Clock Rate	CAN	Package Code	Renesas Part Number	Renesas Order Code	Status
RX62T	256k	32k	16k	D version	Vcc = 4.0 to 5.5V	Yes	PLQP0112JA-A	R5F562TAADFH	R5F562TAADFH#V0	MP
				(-40 -> +85 degrees C)	f = 100MHz	No		R5F562TADDFH	R5F562TADDFH#V0	
						Yes	PLQP0100KB-A	R5F562TAADFP	R5F562TAADFP#V0	
						No		R5F562TADDFP	R5F562TADDFP#V0	
						Yes	PLQP0080JA-A	R5F562TAADFF	R5F562TAADFF#V0	
						No		R5F562TADDFF	R5F562TADDFF#V0	
						Yes	PLQP0064KB-A	R5F562TAADFM	R5F562TAADFM#V0	
						No		R5F562TADDFM	R5F562TADDFM#V0	
					Vcc = 2.7 to 3.6V	Yes	PLQP0112JA-A	R5F562TABDFH	R5F562TABDFH#V0	
					f = 100MHz	No		R5F562TAEDFH	R5F562TAEDFH#V0	
						Yes	PLQP0100KB-A	R5F562TABDFP	R5F562TABDFP#V0	
						No		R5F562TAEDFP	R5F562TAEDFP#V0	
						Yes	PLQP0080JA-A	R5F562TABDFF	R5F562TABDFF#V0	
						No		R5F562TAEDFF	R5F562TAEDFF#V0	
						Yes	PLQP0064KB-A	R5F562TABDFM	R5F562TABDFM#V0	
						No		R5F562TAEDFM	R5F562TAEDFM#V0	
	128k	8k	8k	D version	Vcc = 4.0 to 5.5V	Yes	PLQP0112JA-A	R5F562T7ADFH	R5F562T7ADFH#V0	
				(-40 -> +85 degrees C)	f = 100MHz	No		R5F562T7DDFH	R5F562T7DDFH#V0	
						Yes	PLQP0100KB-A	R5F562T7ADFP	R5F562T7ADFP#V0	
						No		R5F562T7DDFP	R5F562T7DDFP#V0	
						Yes	PLQP0080JA-A	R5F562T7ADFF	R5F562T7ADFF#V0	
						No		R5F562T7DDFF	R5F562T7DDFF#V0	
						Yes	PLQP0064KB-A	R5F562T7ADFM	R5F562T7ADFM#V0	
						No		R5F562T7DDFM	R5F562T7DDFM#V0	
					Vcc = 2.7 to 3.6V	Yes	PLQP0112JA-A	R5F562T7BDFH	R5F562T7BDFH#V0	
					f = 100MHz	No		R5F562T7EDFH	R5F562T7EDFH#V0	
						Yes	PLQP0100KB-A	R5F562T7BDFP	R5F562T7BDFP#V0	
						No		R5F562T7EDFP	R5F562T7EDFP#V0	
						Yes	PLQP0080JA-A	R5F562T7BDFF	R5F562T7BDFF#V0	
						No		R5F562T7EDFF	R5F562T7EDFF#V0	
						Yes	PLQP0064KB-A	R5F562T7BDFM	R5F562T7BDFM#V0	
						No		R5F562T7EDFM	R5F562T7EDFM#V0	
	64k	8k	8k	D version	Vcc = 4.0 to 5.5V	Yes	PLQP0080JA-A	R5F562T6ADFF	R5F562T6ADFF#V0	
				(-40 -> +85 degrees C)	f = 100MHz	No		R5F562T6DDFF	R5F562T6DDFF#V0	
						Yes	PLQP0064KB-A	R5F562T6ADFM	R5F562T6ADFM#V0	
						No		R5F562T6DDFM	R5F562T6DDFM#V0	
					Vcc = 2.7 to 3.6V	Yes	PLQP0080JA-A	R5F562T6BDFF	R5F562T6BDFF#V0	
					f = 100MHz	No		R5F562T6EDFF	R5F562T6EDFF#V0	
						Yes	PLQP0064KB-A	R5F562T6BDFM	R5F562T6BDFM#V0	
						No		R5F562T6EDFM	R5F562T6EDFM#V0	

RX630 Group Quick Reference Sep-12

Device	Flash (bytes)	Data Flash (bytes)	SRAM (bytes)	Operating Temperature	Operating Voltage/ Maximum Clock Rate	CAN	Package Code	Renesas Part Number	Renesas Order Code	Status
RX630	2 Mbytes	32k	128k	D version (-40 -> +85 degrees C)	Vcc=2.7 to 3.6V f = 100MHz	0	PLQP0100KB-A	R5F5630ECDFP	R5F5630ECDFP#V0	on request
						2	PLQP0100KB-A	R5F5630EDDFP	R5F5630EDDFP#V0	MP
						0	PLQP0144KA-A	R5F5630ECDFB	R5F5630ECDFB#V0	on request
						3	PLQP0144KA-A	R5F5630EDDFB	R5F5630EDDFB#V0	MP
						0	PTLG0145KA-A	R5F5630ECDLK	R5F5630ECDLK#U0	on request
						3	PTLG0145KA-A	R5F5630EDDLK	R5F5630EDDLK#U0	MP
						0	PLQP0176KB-A	R5F5630ECDFC	R5F5630ECDFC#V0	on request
						3	PLQP0176KB-A	R5F5630EDDFC	R5F5630EDDFC#V0	MP
						0	PLBG0176GA-A	R5F5630ECDBGR	R5F5630ECDBG#U0	on request
						3	PLBG0176GA-A	R5F5630EDDBGR	R5F5630EDDBG#U0	MP
						0	PTLG0177KA-A	R5F5630ECDLC	R5F5630ECDLC#U0	on request
						3	PTLG0177KA-A	R5F5630EDDLC	R5F5630EDDLC#U0	MP
	1.5 Mbytes	32k	128k	D version (-40 -> +85 degrees C)	Vcc=2.7 to 3.6V f = 100MHz	0	PLQP0100KB-A	R5F5630DCDFP	R5F5630DCDFP#V0	use 2MB for sampling
						2	PLQP0100KB-A	R5F5630DDDFP	R5F5630DDDFP#V0	use 2MB for sampling
						0	PLQP0144KA-A	R5F5630DCDFB	R5F5630DCDFB#V0	use 2MB for sampling
						3	PLQP0144KA-A	R5F5630DDDFB	R5F5630DDDFB#V0	use 2MB for sampling
						0	PTLG0145KA-A	R5F5630DCDLK	R5F5630DCDLK#U0	use 2MB for sampling
						3	PTLG0145KA-A	R5F5630DDDLK	R5F5630DDDLK#U0	use 2MB for sampling
						0	PLQP0176KB-A	R5F5630DCDFC	R5F5630DCDFC#V0	use 2MB for sampling
						3	PLQP0176KB-A	R5F5630DDDFC	R5F5630DDDFC#V0	use 2MB for sampling
						0	PLBG0176GA-A	R5F5630DCDBGR	R5F5630DCDBG#V0	use 2MB for sampling
						3	PLBG0176GA-A	R5F5630DDDBGR	R5F5630DDDBG#V0	use 2MB for sampling
						0	PTLG0177KA-A	R5F5630DCDLC	R5F5630DCDLC#U0	use 2MB for sampling
						3	PTLG0177KA-A	R5F5630DDDLC	R5F5630DDDLC#U0	use 2MB for sampling
	1.0 Mbytes	32k	96k	D version (-40 -> +85 degrees C)	Vcc=2.7 to 3.6V f = 100MHz	0	PLQP0100KB-A	R5F5630BCDFP	R5F5630BCDFP#V0	on request
						2	PLQP0100KB-A	R5F5630BDDFP	R5F5630BDDFP#V0	MP
						0	PLQP0144KA-A	R5F5630BCDFB	R5F5630BCDFB#V0	on request
						2	PLQP0144KA-A	R5F5630BDDFB	R5F5630BDDFB#V0	MP
						0	PTLG0145KA-A	R5F5630BCDLK	R5F5630BCDLK#U0	on request
						2	PTLG0145KA-A	R5F5630BDDLK	R5F5630BDDLK#U0	MP
						0	PLQP0176KB-A	R5F5630BCDFC	R5F5630BCDFC#V0	on request
						2	PLQP0176KB-A	R5F5630BDDFC	R5F5630BDDFC#V0	MP
						0	PLBG0176GA-A	R5F5630BCDBGR	R5F5630BCDBG#V0	on request
						2	PLBG0176GA-A	R5F5630BDDBGR	R5F5630BDDBG#V0	MP
						0	PTLG0177KA-A	R5F5630BCDLC	R5F5630BCDLC#U0	on request
						2	PTLG0177KA-A	R5F5630BDDLC	R5F5630BDDLC#U0	MP
	768 kbytes	32k	96k	D version (-40 -> +85 degrees C)	Vcc=2.7 to 3.6V f = 100MHz	0	PLQP0100KB-A	R5F5630ACDFP	R5F5630ACDFP#V0	use 1MB for sampling
						2	PLQP0100KB-A	R5F5630ADDFP	R5F5630ADDFP#V0	use 1MB for sampling
						0	PLQP0144KA-A	R5F5630ACDFB	R5F5630ACDFB#V0	use 1MB for sampling
						2	PLQP0144KA-A	R5F5630ADDFB	R5F5630ADDFB#V0	use 1MB for sampling
						0	PTLG0145KA-A	R5F5630ACDLK	R5F5630ACDLK#U0	use 1MB for sampling
						2	PTLG0145KA-A	R5F5630ADDLK	R5F5630ADDLK#U0	use 1MB for sampling
						0	PLQP0176KB-A	R5F5630ACDFC	R5F5630ACDFC#V0	use 1MB for sampling
						2	PLQP0176KB-A	R5F5630ADDFC	R5F5630ADDFC#V0	use 1MB for sampling
						0	PLBG0176GA-A	R5F5630ACDBGR	R5F5630ACDBG#V0	use 1MB for sampling
						2	PLBG0176GA-A	R5F5630ADDBGR	R5F5630ADDBG#V0	use 1MB for sampling
						0	PTLG0177KA-A	R5F5630ACDLC	R5F5630ACDLC#U0	use 1MB for sampling
						2	PTLG0177KA-A	R5F5630ADDLC	R5F5630ADDLC#U0	use 1MB for sampling
	512 kbytes	32k	64k	D version (-40 -> +85 degrees C)	Vcc=2.7 to 3.6V f = 100MHz	0	PLQP0100KB-A	R5F56308CDFP	R5F56308CDFP#V0	on request
						1	PLQP0100KB-A	R5F56308DDFP	R5F56308DDFP#V0	MP
						0	PTLG0100KA-A	R5F56308CDLA	R5F56308CDLA#U0	on request
						1	PTLG0100KA-A	R5F56308DDLA	R5F56308DDLA#U0	on request
	384kbytes	32k	64k	D version (-40 -> +85 degrees C)	Vcc=2.7 to 3.6V f = 100MHz	0	PLQP0100KB-A	R5F56307CDFP	R5F56307CDFP#V0	use 512kB for sampling
						1	PLQP0100KB-A	R5F56307DDFP	R5F56307DDFP#V0	use 512kB for sampling
						0	PTLG0100KA-A	R5F56307CDLA	R5F56307CDLA#U0	on request
						1	PTLG0100KA-A	R5F56307DDLA	R5F56307DDLA#U0	on request

RX631 / RX63N Group Quick Reference Sep-12

Device	Flash (bytes)	Data Flash (bytes)	SRAM (bytes)	Operating Temperature	Operating Voltage/ Maximum Clock Rate	Ethernet	CAN	USB (OTG, Host , Function)	Package Code	Renesas Part Number	Renesas Order Code	Status
RX631	2 Mbytes	32k	128k	D version	Vcc=2.7 to 3.6V	0	0	1	PLQP0100KB-A	R5F5631ECDFP	R5F5631ECDFP#V0	on request
				(-40 -> +85 degrees C	f = 100MHz	0	2	1	PLQP0100KB-A	R5F5631EDDFP	R5F5631EDDFP#V0	MP Q3/2012
						0	0	1	PLQP0144KA-A	R5F5631ECDFB	R5F5631ECDFB#V0	on request
						0	3	1	PLQP0144KA-A	R5F5631EDDFB	R5F5631EDDFB#V0	MP Q2/2012
						0	0	1	PTLG0145KA-A	R5F5631ECDLK	R5F5631ECDLK#U0	on request
						0	3	1	PTLG0145KA-A	R5F5631EDDLK	R5F5631EDDLK#U0	MP Q3/2012
						0	0	2	PLQP0176KB-A	R5F5631ECDFC	R5F5631ECDFC#V0	on request
						0	3	2	PLQP0176KB-A	R5F5631EDDFC	R5F5631EDDFC#V0	MP Q3/2012
						0	0	2	PTLG0177JB-A	R5F5631ECDLC	R5F5631ECDLC#U0	on request
						0	3	2	PTLG0177JB-A	R5F5631EDDLC	R5F5631EDDLC#U0	MP Q3/2012
						0	0	2	PLBG0176GA-A	R5F5631ECDBG	R5F5631ECDBG#V0	on request
						0	3	2	PLBG0176GA-A	R5F5631EDDBG	R5F5631EDDBG#V0	MP Q3/2012
	1.5 Mbytes	32k	128k	D version	Vcc=2.7 to 3.6V	0	0	1	PLQP0100KB-A	R5F5631DCDFP	R5F5631DCDFP#V0	use 2MB for sampling
				(-40 -> +85 degrees C	f = 100MHz	0	2	1	PLQP0100KB-A	R5F5631DDDFP	R5F5631DDDFP#V0	use 2MB for sampling
						0	0	1	PLQP0144KA-A	R5F5631DCDFB	R5F5631DCDFB#V0	use 2MB for sampling
						0	3	1	PLQP0144KA-A	R5F5631DDDFB	R5F5631DDDFB#V0	use 2MB for sampling
						0	0	1	PTLG0145KA-A	R5F5631DCDLK	R5F5631DCDLK#U0	use 2MB for sampling
						0	3	1	PTLG0145KA-A	R5F5631DDDLK	R5F5631DDDLK#U0	use 2MB for sampling
						0	0	2	PLQP0176KB-A	R5F5631DCDFC	R5F5631DCDFC#V0	use 2MB for sampling
						0	3	2	PLQP0176KB-A	R5F5631DDDFC	R5F5631DDDFC#V0	use 2MB for sampling
						0	0	2	PTLG0177JB-A	R5F5631DCDLC	R5F5631DCDLC#U0	use 2MB for sampling
						0	3	2	PTLG0177JB-A	R5F5631DDDLC	R5F5631DDDLC#U0	use 2MB for sampling
						0	0	2	PLBG0176GA-A	R5F5631DCDBG	R5F5631DCDBG#V0	use 2MB for sampling
						0	3	2	PLBG0176GA-A	R5F5631DDDBG	R5F5631DDDBG#V0	use 2MB for sampling
	1.0 Mbytes	32k	128k	D version	Vcc=2.7 to 3.6V	0	0	1	PLQP0100KB-A	R5F5631BCDFP	R5F5631BCDFP#V0	use 2MB for sampling
				(-40 -> +85 degrees C	f = 100MHz	0	2	1	PLQP0100KB-A	R5F5631BDDFP	R5F5631BDDFP#V0	use 2MB for sampling
						0	0	1	PLQP0144KA-A	R5F5631BCDFB	R5F5631BCDFB#V0	use 2MB for sampling
						0	2	1	PLQP0144KA-A	R5F5631BDDFB	R5F5631BDDFB#V0	use 2MB for sampling
						0	0	1	PTLG0145KA-A	R5F5631BCDLK	R5F5631BCDLK#U0	use 2MB for sampling
						0	2	1	PTLG0145KA-A	R5F5631BDDLK	R5F5631BDDLK#U0	use 2MB for sampling
						0	0	2	PLQP0176KB-A	R5F5631BCDFC	R5F5631BCDFC#V0	use 2MB for sampling
						0	2	2	PLQP0176KB-A	R5F5631BDDFC	R5F5631BDDFC#V0	use 2MB for sampling
						0	0	2	PTLG0177JB-A	R5F5631BCDLC	R5F5631BCDLC#U0	use 2MB for sampling
						0	2	2	PTLG0177JB-A	R5F5631BDDLC	R5F5631BDDLC#U0	use 2MB for sampling
						0	0	2	PLBG0176GA-A	R5F5631BCDBG	R5F5631BCDBG#V0	use 2MB for sampling
						0	2	2	PLBG0176GA-A	R5F5631BDDBG	R5F5631BDDBG#V0	use 2MB for sampling
	768 kbytes	32k	128k	D version	Vcc=2.7 to 3.6V	0	0	1	PLQP0100KB-A	R5F5631ACDFP	R5F5631ACDFP#V0	use 2MB for sampling
				(-40 -> +85 degrees C	f = 100MHz	0	2	1	PLQP0100KB-A	R5F5631ADDFP	R5F5631ADDFP#V0	use 2MB for sampling
						0	0	1	PLQP0144KA-A	R5F5631ACDFB	R5F5631ACDFB#V0	use 2MB for sampling
						0	2	1	PLQP0144KA-A	R5F5631ADDFB	R5F5631ADDFB#V0	use 2MB for sampling
						0	0	1	PTLG0145KA-A	R5F5631ACDLK	R5F5631ACDLK#U0	use 2MB for sampling
						0	2	1	PTLG0145KA-A	R5F5631ADDLK	R5F5631ADDLK#U0	use 2MB for sampling
						0	0	2	PLQP0176KB-A	R5F5631ACDFC	R5F5631ACDFC#V0	use 2MB for sampling
						0	2	2	PLQP0176KB-A	R5F5631ADDFC	R5F5631ADDFC#V0	use 2MB for sampling
						0	0	2	PTLG0177JB-A	R5F5631ACDLC	R5F5631ACDLC#U0	use 2MB for sampling
						0	2	2	PTLG0177JB-A	R5F5631ADDLC	R5F5631ADDLC#U0	use 2MB for sampling
						0	0	2	PLBG0176GA-A	R5F5631ACDBG	R5F5631ACDBG#V0	use 2MB for sampling
						0	2	2	PLBG0176GA-A	R5F5631PDDFM	R5F5631ADDBG#V0	use 2MB for sampling
	512 kbytes	32k	64k	D version	Vcc=2.7 to 3.6V	0	0	1	PLQP0048KB-A	R5F5631PDDFL	R5F5631PDDFL#V0	MP Q4/2012
				(-40 -> +85 degrees C	f = 100MHz	0	2	1	PLQP0064KB-A	R5F5631PDDFM	R5F5631PDDFM#V0	MP Q4/2012
	384 kbytes	32k	64k	D version	Vcc=2.7 to 3.6V	0	0	1	PLQP0048KB-A	R5F5631NDDFL	R5F5631NDDFL#V0	MP Q4/2012
				(-40 -> +85 degrees C	f = 100MHz	0	2	1	PLQP0064KB-A	R5F5631NDDFM	R5F5631NDDFM#V0	MP Q4/2012
	128 kbytes	32k	64k	D version	Vcc=2.7 to 3.6V	0	0	1	PLQP0048KB-A	R5F5631MDDFL	R5F5631MDDFL#V0	MP Q4/2012
				(-40 -> +85 degrees C	f = 100MHz	0	2	1	PLQP0064KB-A	R5F5631MDDFM	R5F5631MDDFM#V0	MP Q4/2012
RX63N	2 Mbytes	32k	128k	D version	Vcc=2.7 to 3.6V	1	0	1	PLQP0100KB-A	R5F563NECDFP	R5F563NECDFP#V0	on request
				(-40 -> +85 degrees C	f = 100MHz	1	2	1	PLQP0100KB-A	R5F563NEDDFP	R5F563NEDDFP#V0	MP Q3/2012
						1	0	1	PLQP0144KA-A	R5F563NECDFB	R5F563NECDFB#V0	on request
						1	3	1	PLQP0144KA-A	R5F563NEDDFB	R5F563NEDDFB#V0	MP Q2/2012
						1	0	1	PTLG0145KA-A	R5F563NECDLK	R5F563NECDLK#U0	on request
						1	3	1	PTLG0145KA-A	R5F563NEDDLK	R5F563NEDDLK#U0	MP Q3/2012
						1	0	2	PLQP0176KB-A	R5F563NECDFC	R5F563NECDFC#V0	on request
						1	3	2	PLQP0176KB-A	R5F563NEDDFC	R5F563NEDDFC#V0	MP Q3/2012
						1	0	2	PTLG0177JB-A	R5F563NECDLC	R5F563NECDLC#U0	on request
						1	3	2	PTLG0177JB-A	R5F563NEDDLC	R5F563NEDDLC#U0	MP Q3/2012
						1	0	2	PLBG0176GA-A	R5F563NECDBG	R5F563NECDBG#V0	on request
						1	3	2	PLBG0176GA-A	R5F563NEDDBG	R5F563NEDDBG#V0	MP Q3/2012
	1.5 Mbytes	32k	128k	D version	Vcc=2.7 to 3.6V	1	0	1	PLQP0100KB-A	R5F563NDCDFP	R5F563NDCDFP#V0	use 2MB for sampling
				(-40 -> +85 degrees C	f = 100MHz	1	2	1	PLQP0100KB-A	R5F563NDDDFP	R5F563NDDDFP#V0	use 2MB for sampling
						1	0	1	PLQP0144KA-A	R5F563NDCDFB	R5F563NDCDFB#V0	use 2MB for sampling
						1	3	1	PLQP0144KA-A	R5F563NDDDFB	R5F563NDDDFB#V0	use 2MB for sampling
						1	0	1	PTLG0145KA-A	R5F563NDCDLK	R5F563NDCDLK#U0	use 2MB for sampling
						1	3	1	PTLG0145KA-A	R5F563NDDDLK	R5F563NDDDLK#U0	use 2MB for sampling
						1	0	2	PLQP0176KB-A	R5F563NDCDFC	R5F563NDCDFC#V0	use 2MB for sampling
						1	3	2	PLQP0176KB-A	R5F563NDDDFC	R5F563NDDDFC#V0	use 2MB for sampling
						1	0	2	PTLG0177JB-A	R5F563NDCDLC	R5F563NDCDLC#U0	use 2MB for sampling
						1	3	2	PTLG0177JB-A	R5F563NDDDLC	R5F563NDDDLC#U0	use 2MB for sampling
						1	0	2	PLBG0176GA-A	R5F563NDCDBG	R5F563NDCDBG#V0	use 2MB for sampling
						1	3	2	PLBG0176GA-A	R5F563NDDDBG	R5F563NDDDBG#V0	use 2MB for sampling
	1.0 Mbytes	32k	128k	D version	Vcc=2.7 to 3.6V	1	0	1	PLQP0100KB-A	R5F563NBCDFP	R5F563NBCDFP#V0	use 2MB for sampling
				(-40 -> +85 degrees C	f = 100MHz	1	2	1	PLQP0100KB-A	R5F563NBDDFP	R5F563NBDDFP#V0	use 2MB for sampling
						1	0	1	PLQP0144KA-A	R5F563NBCDFB	R5F563NBCDFB#V0	use 2MB for sampling
						1	2	1	PLQP0144KA-A	R5F563NBDDFB	R5F563NBDDFB#V0	use 2MB for sampling
						1	0	1	PTLG0145KA-A	R5F563NBCDLK	R5F563NBCDLK#U0	use 2MB for sampling
						1	2	1	PTLG0145KA-A	R5F563NBDDLK	R5F563NBDDLK#U0	use 2MB for sampling
						1	0	2	PLQP0176KB-A	R5F563NBCDFC	R5F563NBCDFC#V0	use 2MB for sampling
						1	2	2	PLQP0176KB-A	R5F563NBDDFC	R5F563NBDDFC#V0	use 2MB for sampling
						1	0	2	PTLG0177JB-A	R5F563NBCDLC	R5F563NBCDLC#U0	use 2MB for sampling
						1	2	2	PTLG0177JB-A	R5F563NBDDLC	R5F563NBDDLC#U0	use 2MB for sampling
						1	0	2	PLBG0176GA-A	R5F563NBCDBG	R5F563NBCDBG#V0	use 2MB for sampling
						1	2	2	PLBG0176GA-A	R5F563NBDDBG	R5F563NBDDBG#V0	use 2MB for sampling
	768 kbytes	32k	128k	D version	Vcc=2.7 to 3.6V	1	0	1	PLQP0100KB-A	R5F563NACDFP	R5F563NACDFP#V0	TBC
				(-40 -> +85 degrees C	f = 100MHz	1	2	1	PLQP0100KB-A	R5F563NADDFP	R5F563NADDFP#V0	TBC
						1	0	1	PLQP0144KA-A	R5F563NACDFB	R5F563NACDFB#V0	TBC
						1	2	1	PLQP0144KA-A	R5F563NADDFB	R5F563NADDFB#V0	TBC
						1	0	1	PTLG0145KA-A	R5F563NACDLK	R5F563NACDLK#U0	TBC
						1	2	1	PTLG0145KA-A	R5F563NADDLK	R5F563NADDLK#U0	TBC
						1	0	2	PLQP0176KB-A	R5F563NACDFC	R5F563NACDFC#V0	TBC
						1	2	2	PLQP0176KB-A	R5F563NADDFC	R5F563NADDFC#V0	TBC
						1	0	2	PTLG0177JB-A	R5F563NACDLC	R5F563NACDLC#U0	TBC
						1	2	2	PTLG0177JB-A	R5F563NADDLC	R5F563NADDLC#U0	TBC
						1	0	2	PLBG0176GA-A	R5F563NACDBG	R5F563NACDBG#V0	TBC
						1	2	2	PLBG0176GA-A	R5F563NADDBG	R5F563NADDBG#V0	TBC

RX63T Group Quick Reference

Sep-12

Device	Flash (bytes)	Data Flash (bytes)	SRAM (bytes)	Operating Temperature	Operating Voltage/ Maximum Clock Rate	Package Code	Renesas Part Number	Renesas Order Code	Status
RX63T	64 Kbytes	8 Kbytes	8 Kbytes	D version	Vcc=2.7 to 3.6V	PLQP0064KB-A	R5F563T6EDFM	R5F563T6EDFM#V0	MP Q2/2012
				(-40 -> +85 degrees C)	f = 100 MHz	PLQP0048KB-A	R5F563T6EDFL	R5F563T6EDFL#V0	MP Q2/2012
	48 Kbytes					PLQP0064KB-A	R5F563T5EDFM	R5F563T5EDFM#V0	use 64kB for samping
						PLQP0048KB-A	R5F563T5EDFL	R5F563T5EDFL#V0	use 64kB for samping
	32Kbytes					PLQP0064KB-A	R5F563T4EDFM	R5F563T4EDFM#V0	use 64kB for samping
						PLQP0048KB-A	R5F563T4EDFL	R5F563T4EDFL#V0	use 64kB for samping

RX210 Group Quick Reference Sep-12

Device	Flash (bytes)	Data Flash (bytes)	SRAM (bytes)	Operating Temperature	Operating Voltage/ Maximum Clock Rate	Package	Renesas Part Number	Renesas order Code	Status
RX210	1 Mbyte	8k	96k	D version	Vcc=2.7 to 5.5V	PTLG0145KA-A	R5F5210BBDLK	R5F5210BBDLK#U0	Samples Nov '12
					f = 50 MHz				MP 2Q '13
				(-40 -> +85 degrees C)	Vcc= 1.8 to 5.5V	PLQP0144KA-A	R5F5210BBDFB	R5F5210BBDFB#V0	Samples Oct '12
					f = 32 MHz				MP 2Q '13
					Vcc= 1.62 to 5.5V	PLQP0100KB-A	R5F5210BBDFP	R5F5210BBDFP#V0	Samples Oct '12
					f = 20 MHz				MP 2Q '13
						PTLG0100JA-A	R5F5210BBDLJ	R5F5210BBDLJ#U0	Samples Nov '12
									MP 2Q '13
				G version	Vcc=2.7 to 5.5V	PTLG0145KA-A	R5F5210BBGLK	R5F5210BBGLK#U0	TBC
				(-40 -> +105 degrees C	f = 50 MHz				
					Vcc= 1.8 to 5.5V	PLQP0144KA-A	R5F5210BBGFB	R5F5210BBGFB#V0	TBC
					f = 32 MHz				
					Vcc= 1.62 to 5.5V	PLQP0100KB-A	R5F5210BBGFP	R5F5210BBGFP#V0	TBC
					f = 20 MHz	PTLG0100JA-A	R5F5210BBGLJ	R5F5210BBGLJ#U0	TBC
	768k	8k	96k	D version	Vcc=2.7 to 5.5V	PTLG0145KA-A	R5F5210ABDLK	R5F5210ABDLK#U0	MP 2Q '13
					f = 50 MHz				
				(-40 -> +85 degrees C)	Vcc= 1.8 to 5.5V	PLQP0144KA-A	R5F5210ABDFB	R5F5210ABDFB#V0	MP 2Q '13
					f = 32 MHz				
					Vcc= 1.62 to 5.5V	PLQP0100KB-A	R5F5210ABDFP	R5F5210ABDFP#V0	MP 2Q '13
					f = 20 MHz	PTLG0100JA-A	R5F5210ABDLJ	R5F5210ABDLJ#U0	MP 2Q '13
				G version	Vcc=2.7 to 5.5V	PTLG0145KA-A	R5F5210ABGLK	R5F5210ABGLK#U0	TBC
				(-40 -> +105 degrees C	f = 50 MHz				
					Vcc= 1.8 to 5.5V	PLQP0144KA-A	R5F5210ABGFB	R5F5210ABGFB#V0	TBC
					f = 32 MHz				
					Vcc= 1.62 to 5.5V	PLQP0100KB-A	R5F5210ABGFP	R5F5210ABGFP#V0	TBC
					f = 20 MHz	PTLG0100JA-A	R5F5210ABGLJ	R5F5210ABGLJ#U0	TBC
	512k	8k	64k	D version	Vcc=2.7 to 5.5V	PTLG0145KA-A	R5F52108BDLK	R5F52108BDLK#U0	Samples 1Q '13
				(-40 -> +85 degrees C)	f = 50 MHz				MP 2Q '13
					Vcc= 1.8 to 5.5V	PLQP0144KA-A	R5F52108BDFB	R5F52108BDFB#V0	Samples 1Q '13
					f = 32 MHz				MP 2Q '13
					Vcc= 1.62 to 5.5V	PLQP0100KB-A	R5F52108ADFP	R5F52108ADFP#V0	MP
					f = 20 MHz	PLQP0100KB-A	R5F52108CDFP	R5F52108CDFP#V0	Samples Oct '12
									Jan '13
						PTLG0100JA-A	R5F52108ADLJ	R5F52108ADLJ#U0	MP
						PTLG0100JA-A	R5F52108CDLJ	R5F52108CDLJ#U0	Samples Oct '12
									Jan '13
						PLQP0080KB-A	R5F52108ADFN	R5F52108ADFN#V0	MP
						PLQP0080KB-A	R5F52108CDFN	R5F52108CDFN#V0	Samples Oct '12
									Jan '13
						PLQP0080JA-A	R5F52108CDFF	R5F52108CDFF#V0	Samples Nov '12
									2Q '13
						PLQP0064KB-A	R5F52108ADFM	R5F52108ADFM#V0	MP
						PLQP0064KB-A	R5F52108CDFM	R5F52108CDFM#V0	Samples Oct '12
									Jan '13
						PLQP0064GA-A	R5F52108CDFK	R5F52108CDFK#V0	Samples Nov '12
									Feb '13
				G version	Vcc=2.7 to 5.5V	PTLG0145KA-A	R5F52108BDLK	R5F52108BDLK#U0	TBC
				(-40 -> +105 degrees C	f = 50 MHz	PLQP0144KA-A	R5F52108BDFB	R5F52108BDFB#V0	TBC
					Vcc= 1.8 to 5.5V	PLQP0100KB-A	R5F52108CGFP	R5F52108CGFP#V0	TBC
					f = 32 MHz	PTLG0100JA-A	R5F52108CGLJ	R5F52108CGLJ#U0	TBC
					Vcc= 1.62 to 5.5V	PLQP0080KB-A	R5F52108CGFN	R5F52108CGFN#V0	TBC
					f = 20 MHz	PLQP0064KB-A	R5F52108CGFM	R5F52108CGFM#V0	TBC
						PLQP0064GA-A	R5F52108CGFK	R5F52108CGFK#V0	TBC
	384k	8k	64k	D version	Vcc=2.7 to 5.5V	PTLG0145KA-A	R5F52107BDLK	R5F52107BDLK#U0	2Q '13
				(-40 -> +85 degrees C)	f = 50 MHz	PLQP0144KA-A	R5F52107BDFB	R5F52107BDFB#V0	2Q '13
					Vcc= 1.8 to 5.5V	PLQP0100KB-A	R5F52107ADFP	R5F52107ADFP#V0	MP
					f = 32 MHz	PLQP0100KB-A	R5F52107CDFP	R5F52107CDFP#V0	Dec '12
					Vcc= 1.62 to 5.5V	PTLG0100JA-A	R5F52107ADLJ	R5F52107ADLJ#U0	MP
					f = 20 MHz	PTLG0100JA-A	R5F52107CDLJ	R5F52107CDLJ#U0	Dec '12
						PLQP0080KB-A	R5F52107ADFN	R5F52107ADFN#V0	MP
						PLQP0080KB-A	R5F52107CDFN	R5F52107CDFN#V0	Dec '12
						PLQP0080JA-A	R5F52107CDFF	R5F52107CDFF#V0	2Q '13
						PLQP0064KB-A	R5F52107ADFM	R5F52107ADFM#V0	MP
						PLQP0064KB-A	R5F52107CDFM	R5F52107CDFM#V0	Dec '12
						PLQP0064GA-A	R5F52107CDFK	R5F52107CDFK#V0	Jan '13
				G version	Vcc=2.7 to 5.5V	PTLG0145KA-A	R5F52107BDLK	R5F52107BDLK#U0	TBC
				(-40 -> +105 degrees C	f = 50 MHz	PLQP0144KA-A	R5F52107BDFB	R5F52107BDFB#V0	TBC
					Vcc= 1.8 to 5.5V	PLQP0100KB-A	R5F52107CGFP	R5F52107CGFP#V0	TBC
					f = 32 MHz	PTLG0100JA-A	R5F52107CGLJ	R5F52107CGLJ#U0	TBC
					Vcc= 1.62 to 5.5V	PLQP0080KB-A	R5F52107CGFN	R5F52107CGFN#V0	TBC
					f = 20 MHz	PLQP0064KB-A	R5F52107CGFM	R5F52107CGFM#V0	TBC
						PLQP0064GA-A	R5F52107CGFK	R5F52107CGFK#V0	TBC

RX210 Group Quick Reference Sep-12

Device	Flash (bytes)	Data Flash (bytes)	SRAM (bytes)	Operating Temperature	Operating Voltage/ Maximum Clock Rate	Package	Renesas Part Number	Renesas order Code	Status
	256k	8k	32k	D version	Vcc=2.7 to 5.5V	PTLG0145KA-A	R5F52106BDLK	R5F52106BDLK#U0	2Q '13
				(-40 -> +85 degrees C)	f = 50 MHz	PLQP0144KA-A	R5F52106BDFB	R5F52106BDFB#V0	2Q '13
					Vcc= 1.8 to 5.5V	PLQP0100KB-A	R5F52106ADFP	R5F52106ADFP#V0	MP
									Sampling
					f = 32 MHz	PLQP0100KB-A	R5F52106BDFP	R5F52106BDFP#V0	MP Dec '12
					Vcc= 1.62 to 5.5V	PTLG0100JA-A	R5F52106ADLJ	R5F52106ADLJ#U0	MP
									Sampling
					f = 20 MHz	PTLG0100JA-A	R5F52106BDLJ	R5F52106BDLJ#U0	MP Dec '12
						PLQP0080KB-A	R5F52106ADFN	R5F52106ADFN#V0	MP
									Sampling
						PLQP0080KB-A	R5F52106BDFN	R5F52106BDFN#V0	MP Dec '12
						PLQP0080JA-A	R5F52106CDFF	R5F52106CDFF#V0	2Q '13
						PLQP0064KB-A	R5F52106ADFM	R5F52106ADFM#V0	MP
									Sampling
						PLQP0064KB-A	R5F52106BDFM	R5F52106BDFM#V0	MP Dec '12
									Sampling
						PLQP0064GA-A	R5F52106BDFK	R5F52106BDFK#V0	MP Feb '13
									Sampling
						PLQP0048KB-A	R5F52106BDFL	R5F52106BDFL#V0	MP Dec '12
				G version	Vcc=2.7 to 5.5V	PTLG0145KA-A	R5F52106BDLK	R5F52106BDLK#U0	TBC
				(-40 -> +105 degrees C	f = 50 MHz	PLQP0144KA-A	R5F52106BDFB	R5F52106BDFB#V0	TBC
					Vcc= 1.8 to 5.5V	PLQP0100KB-A	R5F52106BGFP	R5F52106BGFP#V0	TBC
					f = 32 MHz	PTLG0100JA-A	R5F52106BGLJ	R5F52106BGLJ#U0	TBC
					Vcc= 1.62 to 5.5V	PLQP0080KB-A	R5F52106BGFN	R5F52106BGFN#V0	TBC
					f = 20 MHz	PLQP0064KB-A	R5F52106BGFM	R5F52106BGFM#V0	TBC
						PLQP0064GA-A	R5F52106BGFK	R5F52106BGFK#V0	TBC
						PLQP0048KB-A	R5F52106BGFL	R5F52106BGFL#V0	TBC
	128k	8k	20k	D version	Vcc=2.7 to 5.5V	PTLG0145KA-A	R5F52105BDLK	R5F52105BDLK#U0	2Q '13
				(-40 -> +85 degrees C)	f = 50 MHz	PLQP0144KA-A	R5F52105BDFB	R5F52105BDFB#V0	2Q '13
					Vcc= 1.8 to 5.5V	PLQP0100KB-A	R5F52105ADFP	R5F52105ADFP#V0	MP
					f = 32 MHz	PLQP0100KB-A	R5F52105BDFP	R5F52105BDFP#V0	MP Dec '12
					Vcc= 1.62 to 5.5V	PTLG0100JA-A	R5F52105ADLJ	R5F52105ADLJ#U0	MP
					f = 20 MHz	PTLG0100JA-A	R5F52105BDLJ	R5F52105BDLJ#U0	MP Dec '12
						PLQP0080KB-A	R5F52105ADFN	R5F52105ADFN#V0	MP
						PLQP0080KB-A	R5F52105BDFN	R5F52105BDFN#V0	MP Dec '12
						PLQP0080JA-A	R5F52105CDFF	R5F52105CDFF#V0	2Q '13
						PLQP0064KB-A	R5F52105ADFM	R5F52105ADFM#V0	MP
						PLQP0064KB-A	R5F52105BDFM	R5F52105BDFM#V0	MP Dec '12
						PLQP0064GA-A	R5F52105BDFK	R5F52105BDFK#V0	MP Feb '13
						PLQP0048KB-A	R5F52105BDFL	R5F52105BDFL#V0	MP Dec '12
				G version	Vcc=2.7 to 5.5V	PTLG0145KA-A	R5F52105BDLK	R5F52105BDLK#U0	TBC
				(-40 -> +105 degrees C	f = 50 MHz	PLQP0144KA-A	R5F52105BDFB	R5F52105BDFB#V0	TBC
					Vcc= 1.8 to 5.5V	PLQP0100KB-A	R5F52105BGFP	R5F52105BGFP#V0	TBC
					f = 32 MHz	PTLG0100JA-A	R5F52105BGLJ	R5F52105BGLJ#U0	TBC
					Vcc= 1.62 to 5.5V	PLQP0080KB-A	R5F52105BGFN	R5F52105BGFN#V0	TBC
					f = 20 MHz	PLQP0064KB-A	R5F52105BGFM	R5F52105BGFM#V0	TBC
						PLQP0064GA-A	R5F52105BGFK	R5F52105BGFK#V0	TBC
						PLQP0048KB-A	R5F52105BGFL	R5F52105BGFL#V0	TBC
	96k	8k	16k	D version	Vcc=2.7 to 5.5V	PLQP0064GA-A	R5F52104BDFK	R5F52104BDFK#V0	MP Dec '12
				(-40 -> +85 degrees C)	f = 50 MHz				
					Vcc= 1.8 to 5.5V	PLQP0048KB-A	R5F52104BDFL	R5F52104BDFL#V0	MP Feb '13
					f = 32 MHz				
					Vcc= 1.62 to 5.5V				
					f = 20 MHz				
				G version	Vcc=2.7 to 5.5V	PLQP0064GA-A	R5F52104BGFK	R5F52104BGFK#V0	TBC
				(-40 -> +105 degrees C	f = 50 MHz				
					Vcc= 1.8 to 5.5V	PLQP0048KB-A	R5F52104BGFL	R5F52104BGFL#V0	TBC
					f = 32 MHz				
					Vcc= 1.62 to 5.5V				
					f = 20 MHz				
	64k	8k	12k	D version	Vcc=2.7 to 5.5V	PLQP0064GA-A	R5F52103BDFK	R5F52103BDFK#V0	MP Dec '12
				(-40 -> +85 degrees C)	f = 50 MHz				
					Vcc= 1.8 to 5.5V	PLQP0048KB-A	R5F52103BDFL	R5F52103BDFL#V0	MP Feb '13
					f = 32 MHz				
					Vcc= 1.62 to 5.5V				
					f = 20 MHz				
				G version	Vcc=2.7 to 5.5V	PLQP0064GA-A	R5F52103BGFK	R5F52103BGFK#V0	TBC
				(-40 -> +105 degrees C	f = 50 MHz				
					Vcc= 1.8 to 5.5V	PLQP0048KB-A	R5F52103BGFL	R5F52103BGFL#V0	TBC
					f = 32 MHz				
					Vcc= 1.62 to 5.5V				
					f = 20 MHz				

Please contact PM for sample availability

RX210 Mask Description

A Mask: RX210 128k, 256k, 384k & 512k only, 100 pin package or less, original mask set, with software based flash control Unit, and issues with main clock oscillator input and 5v tolerance on I/O pins
B Mask: All other Rx200 apart from RX210 128k, 256k, 384k & 512k only, 100 pin package or less, true hardware Flash Control Unit, additional low power modes to improve low speed performance, no problems with main external oscillator or 5v tolerance
C Mask: RX210 with 384k & 512k flash only, 100 pin package or less, no problems with main external oscillator or 5v tolerance

Please use B or C mask when available

RX220 Group Quick Reference Sep-12

Device	Flash (bytes)	Data Flash (bytes)	SRAM (bytes)	Operating Temperature	Operating Voltage/ Maximum Clock Rate	Package	Renesas Part Number	Renesas order Code	Status
RX220	256k	8k	16K	D version	Vcc= 1.8 to 5.5V	PLQP0100KB-A	R5F52206BDFP	R5F52206BDFP#V0	Sampling MP 4Q '12
				(-40 -> +85 degrees C)	f = 32 MHz	PLQP0064KB-A	R5F52206BDFM	R5F52206BDFM#V0	Sampling MP 4Q '12
					Vcc= 1.8 to 5.5V	PLQP0064GA-A	R5F52206BDFK	R5F52206BDFK#V0	TBC
					f = 8 MHz	PLQP0048KB-A	R5F52206BDFL	R5F52206BDFL#V0	Sampling MP 4Q '12
					Vcc= 1.62 to 5.5V				
					f = 4 MHz				
				G version	TBC	PLQP0100KB-A	R5F52206BGFP	R5F52206BGFP#V0	TBC
				(-40 -> +105 degrees C)		PLQP0064KB-A	R5F52206BGFM	R5F52206BGFM#V0	TBC
						PLQP0064GA-A	R5F52206BGFK	R5F52206BGFK#V0	TBC
						PLQP0048KB-A	R5F52206BGFL	R5F52206BGFL#V0	TBC
	128k	8k	8k	D version	Vcc= 1.8 to 5.5V	PLQP0100KB-A	R5F52205BDFP	R5F52205BDFP#V0	4Q '12
				(-40 -> +85 degrees C)	f = 32 MHz	PLQP0064KB-A	R5F52205BDFM	R5F52205BDFM#V0	4Q '12
					Vcc= 1.8 to 5.5V	PLQP0064GA-A	R5F52205BDFK	R5F52205BDFK#V0	TBC
					f = 8 MHz	PLQP0048KB-A	R5F52205BDFL	R5F52205BDFL#V0	4Q '12
					Vcc= 1.62 to 5.5V				
					f = 4 MHz				
				G version	TBC	PLQP0100KB-A	R5F52205BDFP	R5F52205BGFP#V0	TBC
				(-40 -> +105 degrees C)		PLQP0064KB-A	R5F52205BDFM	R5F52205BGFM#V0	TBC
						PLQP0064GA-A	R5F52205BDFK	R5F52205BGFK#V0	TBC
						PLQP0048KB-A	R5F52205BDFL	R5F52205BGFL#V0	TBC
	64k	8k	8k	D version	Vcc= 1.8 to 5.5V	PLQP0100KB-A	R5F52203BDFP	R5F52203BDFP#V0	4Q '12
				(-40 -> +85 degrees C)	f = 32 MHz	PLQP0064KB-A	R5F52203BDFM	R5F52203BDFM#V0	4Q '12
					Vcc= 1.8 to 5.5V	PLQP0064GA-A	R5F52203BDFK	R5F52203BDFK#V0	TBC
					f = 8 MHz	PLQP0048KB-A	R5F52203BDFL	R5F52203BDFL#V0	4Q '12
					Vcc= 1.62 to 5.5V				
					f = 4 MHz				
				G version	TBC	PLQP0100KB-A	R5F52203BGFP	R5F52203BGFP#V0	TBC
				(-40 -> +105 degrees C)		PLQP0064KB-A	R5F52203BGFM	R5F52203BGFM#V0	TBC
						PLQP0064GA-A	R5F52203BGFK	R5F52203BGFK#V0	TBC
						PLQP0048KB-A	R5F52203BGFL	R5F52203BGFL#V0	TBC
	32k	8k	4k	D version	Vcc= 1.8 to 5.5V	PLQP0064KB-A	R5F52201BDFM	R5F52201BDFM#V0	4Q '12
				(-40 -> +85 degrees C)	f = 32 MHz	PLQP0064GA-A	R5F52201BDFK	R5F52201BDFK#V0	TBC
					Vcc= 1.8 to 5.5V	PLQP0048KB-A	R5F52201BDFL	R5F52201BDFL#V0	4Q '12
					f = 8 MHz				
					Vcc= 1.62 to 5.5V				
					f = 4 MHz				
				G version	TBC	PLQP0064KB-A	R5F52201BGFM	R5F52201BGFM#V0	TBC
				(-40 -> +105 degrees C)		PLQP0064GA-A	R5F52201BGFK	R5F52201BGFK#V0	TBC
						PLQP0048KB-A	R5F52201BGFL	R5F52201BGFL#V0	TBC

Please contact PM for sample availability

RX21A Group Quick Reference Sep-12

Device	Flash (bytes)	Data Flash (bytes)	SRAM (bytes)	Operating Temperature	Operating Voltage/ Maximum Clock Rate	24-bit ADC Modules	Package	Renesas Part Number	Renesas order Code	Status
RX21A	512k	8k	64k	D version	Vcc=2.7 to 3.6V	7	PLQP0100KB-A	R5F521A8BDFP	R5F521A8BDFP#V0	Sampling
										MP 1Q
				(-40 -> +85 degrees C)	f = 50 MHz	4	PLQP0080KB-A	R5F521A8BDFN	R5F521A8BDFN#V0	Aug '12
										MP 1Q
					Vcc= 1.8 to 3.6V	3	PLQP0064KB-A	R5F521A8BDFM	R5F521A8BDFM#V0	Sampling
					f = 25 MHz					MP 1Q
				G version	TBC	7	PLQP0100KB-A	R5F521A8BGFP	R5F521A8BGFP#V0	TBC
				(-40 -> +105 degrees C)		4	PLQP0080KB-A	R5F521A8BGFN	R5F521A8BGFN#V0	TBC
						3	PLQP0064KB-A	R5F521A8BGFM	R5F521A8BGFM#V0	TBC
	384k	8k	64k	D version	Vcc=2.7 to 3.6V	7	PLQP0100KB-A	R5F521A7BDFP	R5F521A7BDFP#V0	1Q '13
				(-40 -> +85 degrees C)	f = 50 MHz	4	PLQP0080KB-A	R5F521A7BDFN	R5F521A7BDFN#V0	1Q '13
					Vcc= 1.8 to 3.6V	3	PLQP0064KB-A	R5F521A7BDFM	R5F521A7BDFM#V0	1Q '13
					f = 25 MHz					
				G version	TBC	7	PLQP0100KB-A	R5F521A7BGFP	R5F521A7BGFP#V0	TBC
				(-40 -> +105 degrees C)		4	PLQP0080KB-A	R5F521A7BGFN	R5F521A7BGFN#V0	TBC
						3	PLQP0064KB-A	R5F521A7BGFM	R5F521A7BGFM#V0	TBC
	256k	8k	32k	D version	Vcc=2.7 to 3.6V	7	PLQP0100KB-A	R5F521A6BDFP	R5F521A6BDFP#V0	1Q '13
				(-40 -> +85 degrees C)	f = 50 MHz	4	PLQP0080KB-A	R5F521A6BDFN	R5F521A6BDFN#V0	1Q '13
					Vcc= 1.8 to 3.6V	3	PLQP0064KB-A	R5F521A6BDFM	R5F521A6BDFM#V0	1Q '13
					f = 25 MHz					
				G version	TBC	7	PLQP0100KB-A	R5F521A6BGFP	R5F521A6BGFP#V0	TBC
				(-40 -> +105 degrees C)		4	PLQP0080KB-A	R5F521A6BGFN	R5F521A6BGFN#V0	TBC
						3	PLQP0064KB-A	R5F521A6BGFM	R5F521A6BGFM#V0	TBC

Please contact PM for sample availabili

RX Family Quick Reference (Latest Users Manuals) September-12

Device	Included Devices	Type	Name (These Manuals can be downloaded from www.renesas.eu)	Revision Number	Revision Date	Status
RX	RX600 & RX200	Software Manual	RX Family Software User's Manual	1.00	June '10	Available
RX610	RX610	Hardware Manual	RX610 Group Hardware Manual	1.10	April '11	Available
RX610	RX610	Datasheet	RX610 Group Datasheet	1.00	April '11	Available
RX62N	RX621 , RX62N	Hardware Manual	RX62N, RX621 Group Hardware Manual	1.30	Dec '11	Available
RX62N	RX621 , RX62N	Datasheet	RX62N, RX621 Group Datasheet	1.30	Jan '12	Available
RX62T	RX62T	Hardware Manual	RX62T Group Hardware Manual	1.10	April '11	Available
RX62T	RX62T	Datasheet	RX62T Group Datasheet	1.00	April '11	Available
RX62G	RX62G	Hardware Manual	RX62G Group Hardware Manual	1.00	April '12	Available
RX630	RX630	Hardware Manual	RX630 Group Hardware Manual	1.20	Mar '12	Available
RX630	RX630	Datasheet	RX630 Group Datasheet	0.50	May '11	Available
RX63N	RX63N, RX631	Hardware Manual	RX63N, RX631 Group Hardware Manual	1.00	Mar '12	Available
RX63N	RX63N, RX631	Datasheet	RX63N, RX631 Group Datasheet	1.00	Jun'12	Available
RX63T	RX63T-L	Hardware Manual	RX63T Group Hardware Manual	1.00	May'12	Available
RX210	RX210	Hardware Manual	RX210 Group Hardware Manual	1.10	Feb '12	Available
RX210	RX210	Datasheet	RX210 Group Datasheet	1.10	Feb '12	Available
RX220	RX220	Hardware Manual	RX220 Group Hardware Manual	0.51	May '12	Available
RX21A	RX21A	Hardware Manual	RX21A Group Hardware Manual	0.50	April '12	Contact PM

RX Family Quick Reference (Application Notes) Sep-12

Device	Type	Application Note Name (These application notes can be downloaded from www.eu.Renesas.com)	Status
RX600	Flash	Simple Flash API for RX600	OK
RX600	Many	RX610, RX62N, RX621 Group Quick Design Guide	OK
RX600	CPU	Rx600 IO Register Access Latency	OK
RX600	CPU	Rx Family Software Code and Endian Effects	OK
RX600	CPU	Rx Family Signal Processing Library	OK
RX600	CPU	Rx CAN Application Programming Interface	OK
RX600	CPU	RX600 How to Setup and Run CoreMark on a MCU	OK
Rx600	EEPROM	RX600 Virtual EEPROM for RX	OK
Rx600	CAN	RX Family CAN Application Programming Interface	OK
Rx600	CAN	RX600 Series Flash over CAN	OK
Rx600	CAN	RX600 Series CAN-UART Bridge	OK
Rx600	USB	RX USB Host Human Interface Device Class Driver (HHID)	OK
RX610	FPU	Rx610 RX-Stick Audio Processing Demonstration	OK
RX610	CPU	Rx610 Group Vector Table Address Switching	OK
RX610	IIC	RX610 Group RIIC Single-Master Communication Application Note	OK
RX610	IIC	Rx610 Communication with EEPROM	OK
RX610	IIC	RX610 RIIC Multi-Master Communication	OK
RX610	Low Power	Rx610 Group Deep Software Standby Mode	OK
RX610	SCI	Rx610 Group Asynchronous SCI Transmission Using DTC (Little Endian)	OK
RX610	SCI	Rx610 Group Asynchronous SCI Transmission Using DTC (Big Endian)	OK
RX610	SCI	Rx610 Group Asynchronous SCI Transmission Using DMAC	OK
RX610	SCI	Rx610 Group Asynchronous SCI Transmission Reception Using DTC	OK
RX610	SCI	RX610 Group Asynchronous SCI Transmission Reception Using DTC (Big Endian)	OK
RX610	SCI	Rx610 Group Asynchronous SCI Transmission Reception Using DMAC	OK
RX610	SCI	Rx610 Group Asynchronous SCI Reception Using DTC (Little Endian)	OK
RX610	SCI	Rx610 Group Asynchronous SCI Reception Using DTC (Big Endian)	OK
RX610	SCI	RX610 Group Asynchronous SCI Reception Using DMAC	OK
RX610	Memory	Rx610 8_16-Bit SRAM Connection and Access Example	OK
RX610	Power	Rx610 Notes on Analog Power Supply Printed Circuit Board Patterns	OK
RX610	Flash	Rx610 On-chip Flash Memory Reprogramming in Single Chip Mode	OK
RX610	Flash	Rx610 On-chip Flash Memory Reprogramming in Single Chip Mode (slave)	OK
RX610	Flash	Rx610 On-chip Flash Memory Reprogramming in the User Boot Mode (slave)	OK
RX610	Flash	Rx610 On-chip Flash Memory Reprogramming in the User Boot Mode.	OK
RX610	Demo	Rx610 RX-Stick Dhrystone Benchmark	OK
RX610	Demo	Rx610 RX-Stick FPU Bouncing Ball Demo	OK
RX610	Demo	Rx610 Usage of Renesas Starter Kit for RX610 Middleware	OK
RX610	SCI	Rx610 Example of Pulse Output Using TMR (Cascade Connection)	OK
RX610	E1	E1 On-Chip Debugging Emulator. Introductory guide for the Rx610	OK
RX610	CRC	RX610 CRC Code calculation by using DTC Block transfer and async serial comms	OK
RX610	Code	RX610 RSK sample code	OK
RX610	Code	RX600 Series Usage of Renesas Starter Kit for RX610 Middleware Evaluation Board	OK
RX610	Code	RX610,RX62N,RX621 Group Quick Design Guide	OK
RX62N	Stack	Rx62N uIP TCP_IP Protocol Stack Demonstration	OK
RX62N	Flash	RX62N Group Flash over CAN	OK
RX62N	SCI	Rx62N Serial Communications Interface Migration Guide H8SX1668 to RX62N	OK
RX62N	SCI	Rx62N SCI Migration Guide M16C_62P to RX62N	OK
RX62N	ADC	Rx62N M16C62P ADC Migration Guide M16C_62P to RX62N	OK
RX62N	ADC	Rx62N ADC Migration Guide H8SX1668 to RX62N	OK
RX62N	WDT	Rx62N H8SX1668 Watchdog Timer Migration Guide H8SX1668 to RX62N	OK
RX62N	DTC	Rx62N H8SX1668 DTC Migration Guide H8SX1668 to RX62N	OK
RX62N	Timer	Rx62N H8SX1668 8-Bit Timer (TMR) Migration Guide H8SX1668 to RX62N	OK
RX62N	Power	Rx62N 621 Notes on Analog Power Supply Printed Circuit Board Patterns	OK
RX62N	Ethernet	Rx62N Ethernet Transmit and Receive Settings	OK
RX62N	Flash	Rx62N On-chip Flash Memory Reprogramming in Single Chip Mode	OK
RX62N	Flash	Rx62N On-chip Flash Memory Reprogramming in the User Boot Mode	OK
RX62N	Demo	Rx62N Peripheral LibUSB Demo	OK
RX62N	USB	Rx62N Renesas USB Device basic Firmware	OK
RX62N	USB	Rx62N RX62N USB Bootloader with XModem Transfer	OK
RX62N	Demo	Rx62N Sample Code and Workspace Projects Guide	OK
RX62N	USB	RX62N USB Host Communication Device Class Driver (HCDC)	OK
RX62N	USB	Rx62N USB Host Human Interface Device Class Driver (HHID)	OK
RX62N	USB	RX62N USB Peripheral Communications Device Class Driver (PCDC)	OK
RX62N	USB	Rx62N USB Peripheral Mass Storage Class Driver	OK
RX62N	SCI	Rx62N_621 Asynchronous SCIa Communication Using the DTCa Module	OK
RX62N	SCI	Rx62N_621 Asynchronous SCIa TransmissionReception	OK
RX62N	IIC	Rx62N_621 Communication with EEPROM using IIC	OK
RX62N	IIC	RX62N_621 RIIC Multi-master Communications	OK
RX62N	DTC	Rx62N_621 CRC Code Calculation Using DTCa Block Transfer and Asynchronous	OK
RX62N	DTC	Rx62N_621 CRC Code Calculation Using DTCa Block Transfer and Asynchronous	
RX62N	Analogue	Notes on Analog Power Supply Printed Circuit Board Patterns	OK
RX62N	USB	USB Host Human Interface Device Class Driver (HHID)	OK
RX62N	USB	USB Host Communication Device Class Driver Short Sheet.	OK
RX62N	USB	USB Host Mass Storage Class Driver Short Sheet	OK
RX62N	USB	USB Basic Firmware Short Sheet	OK
RX62N	USB	RX62N_621 USB Host Flash Boot Loader	OK
RX62N	Ether	RX62N_621 Ethernet Flash Boot Loader Using M3S-T4-Tiny	OK

RX Family Quick Reference (Application Notes) Sep-12

Device	Type	Application Note Name	Status
		(These application notes can be downloaded from www.eu.Renesas.com)	
RX62N	code	Sample Code and Workspace Projects Guide	OK
RX62N	code	RX62N Multimedia Demonstration on RI600 & FreeRTOS™	OK
RX62N	code	RX62N OS Adaptor Layer RI600 and FreeRTOS™ Blinky Demonstration	OK
RX62N	code	RX62N RX62N Sending iO Push Notifications to an iPhone, Ipod touch or Ipad	OK
RX62N	code	RX62N User Manual and API Specification for RI600 & FreeRTOS™	OK
RX62N	Memory	RX62N_621 32-bit SDRAM connection	OK
RX62N	RSPI	RX62N_RX621 I2S Communication Using RSPI, DTCa, and MTU2	OK
RX62T	MTU3	MTU3 Complementary PWM Mode	OK
RX62T	MTU3	MTU3 Reset-Synchronized PWM Mode	OK
RX62T	SCI	RX62T Asynchronous SCIb Communication Using the DTC Module	OK
RX62T	IIC	Rx62T Communication with EEPROM using RIIC bus	OK
RX62T	IIC	RX62T RIIC Multi-Master Communication	OK
RX62T	Timer	RX62T GPT Three Phase Saw Wave Complementary PWM	OK
RX62T	Timer	RX62T MTU3 Phase Counting Mode	OK
RX62T	ADC	RX62T Notes on Analog Power Supply Printed Circuit Board Patterns	OK
RX62T	Motor	RX62T Single Shunt Sensorless Vector Control of PMSM Motor with Internal Programmable Gain Amplif	OK
RX62T	Motor	RX62T Single Shunt Sensorless Vector Control of PMSM Motors	OK
RX62T	Motor	RX62T Position Control of PMSM Motors with Encoder	OK
RX62T	Motor	RX62T Three Shunt Sensorless Vector Control of PMSM Motor with Internal Programmable Gain Amplif	OK
RX62T	Motor	RX62T Three Shunt Sensorless Vector Control of PMSM Motors	OK
RX62T	Motor	RX62T 120-degree Trapezoidal-wave with Sensorless	OK
RX62T	Motor	RX62T 180-degree Sinusoidal-wave with Hall sensor	OK
RX62T	Motor	RX62T 120-degree Trapezoidal-wave with Hall Sensor	OK
RX62T	Motor	RX62T Stepping Motor Employing Two-Phase Excitation	OK
RX62T	Motor	RX62T Brushed DC Motor Control with IR Compensation	OK
RX62T	Motor	RX62T Usage Example of 12-Bit AD Converter in One Shunt Current Detection Method	OK
RX62T	safety	IEC60730 Self Test Code for RX62T Group MCU	OK
RX62T	ADC	RX62T Group Usage Example of 12-Bit AD Converter in One Shunt Current Detection Method	OK
RX630	RTC	Rx630 100 year calendar (Real-time clock and temperature sensor application)	OK
RX630	Memory	Rx630 Communication with Flash Memory Using Simple SPI	OK
RX630	Low Power	RX630 Deep Software Standby Mode	OK
RX630	I2C	RX630 Group Communication with EEPROM Using Simple I2C	OK
RX630	WDT	Rx630 Independent Watchdog Timer (Autostart Mode)	OK
RX630	Intterupt	Rx630 Interrupt Request Grouping (Level Detection Interrupt Requests)	OK
RX630	protect	RX630 Group Register Write Protection (Clock selection and operation mode settings)	OK
RX630	WDT	RX630 Group Independent Watchdog Timer (Autostart Mode)	OK
RX630	SPI	RX630 Group Communication with Flash Memory Using the Serial Communications Interface (Simple SI	OK
RX630	I2C	RX630 Group Communication with EEPROM Using the Serial Communications Interface (Simple I2C)	OK
RX630	Standby	RX630 Group Deep Software Standby Mode	OK
RX630	SCI	RX630 Group Interrupt-mode UART Driver for RX630	OK
RX630	Interrupts	RX630 Group Interrupt Request Grouping (Level Detection Interrupt Requests)	OK
RX63N	Clock	RX63N/RX631 Groups Design Guide for Low CL Sub-Clock Circuits	OK
RX63N	Timer	RX63N Group Simple Timer Framework	OK
RX63N	comms	RX63N Zero copy ethernet driver demonstration	OK
RX210	Code	Simple Flash API for RX200	OK
RX210	Code	RX210_Sample_Code	OK
RX210	Safety	RX200 Series VDE Certified IEC60730 Self Test Code for RX200 Series MCU	OK
RX210	Oscillator	Rx210 Design Guide for Low CL Sub-Clock Circuits	OK
RX210	Code	RX210 initial setting	OK
RX210	Memory	Rx210 external memory apps note	OK
RX210	ELC	Rx200 Using the ELC to link the LVD and LOCO	OK
RX210	memory	Rx200 Virtual EEPROM	OK
RX	Compiler	Rx Compiler Tips for Efficient Programming Edition Apps note	OK
RX	Compiler	Rx Family C Compiler Application Notes Sample Project RX Migration Guide, H8 Edition	OK
RX	Compiler	Rx Family C Compiler Application Notes Sample Project RX Migration Guide SH Edition	OK
RX	Compiler	Rx Family C Compiler Application Notes Sample Project RX Migration Guide, M16C Edition	OK
RX	Compiler	Rx Family C Compiler Application Notes Installation Guide - Renesas IDE Startup Guide	OK
RX	Compiler	Rx Family C Compiler Application Notes Compiler Usage Guide Languages Edition (C89, C99)	OK
RX	Compiler	Rx Family C Compiler Application Note Compiler Usage Guide	OK
RX	Compiler	Rx Family C Compiler Application Note Compiler Usage Guide, Option Edition	OK
RX	Compiler	Rx Family C Compiler Application Note [Compiler Use Guide], #pragma Extension Guide	OK
RX	Compiler	Rx Family C Compiler Application Note RX Migration Guide, M16C Edition	OK
RX	Compiler	Rx Family C Compiler Application Note RX Migration Guide, H8 Edition	OK
RX	Compiler	Rx Family C Compiler Application Note Reference Data of Library	OK
RX	Compiler	RX Adding Printf and Scanf Support	OK
RX	DSP	RX Application Example of Multiple precision Arithmetic Computing Mersenne Primes	OK
RX	DSP	Rx Color Space Conversion Using the DSP Instructions	OK
RX	DSP	RX Complex Number Operation Programming with DSP Function Instructions	OK
RX	DSP	Rx Family Signal Processing Library	OK
RX	DSP	Rx Image Filter Programs Leveraging a DSP Instruction.	OK
RX	DSP	RX Matrix Operation Programming with DSP Function Instructions	OK
RX	DSP	RX Multiple precision Multiplication Program Making Use of the DSP Functions.	OK
RX	DSP	RX Using Intrinsic Functions for Multiply-Accumulate Operations	OK
RX	DSP	Using Multiply-Accumulate Operation Instructions	OK
RX	Software	Register Write Protection (clock selection and operation mode settings)	OK
RX	Software	RX62N Renesas Starter Kit Sample Code	OK
RX	Software	CAN Application Programming Interface	OK
RX	Software	Running Coremark on RX	OK
RX	Coding	RX Using RPDL & PDG	OK

RX Family Quick Reference (Technical Updates) September-12

Device	Included Devices	Type	Reference Document	Update Number	Decsription (These Updates can be downloaded from www.eu.Renesas.com)	Release Date	Revision Number	Status
RX610	RX610	Techical Note	RX610 Hardware Manual	TN-RX*-A001A/E	Notes on Compare Match Timer (CMT)	Aug '10	1.0	OK
RX610	RX610	Technical Note	RX610 Hardware Manual	TN-RX*-A002A/E	Notes while using multiple units of A/D converter and while using D/A converter	Aug '10	1.0	OK
RX610	RX610	Technical Note	RX610 Hardware Manual	TN-RX*-A003A/E	Notes on DMAC/DTC transfer using Communication Function	Aug '10	1.0	OK
RX610	RX610	Technical Note	RX610 Hardware Manual	TN-RX*-A004A/E	Error Correction of the table List of Pins and Pin Functions (176-Pin LFBGA)	Aug '10	1.0	OK
RX600	RX610, RX62T, RX621, RX62N	Technical Note	many	TN-RX*-A005A/E	The correction of errors in the I2C Bus Interface (RIIC) Hardware Manual	Apr '11	1.0	OK
RX62T	RX62T	Technical Note	RX62T Hardware Manual	TN-RX*-A006A/E	Points for Caution Regarding the General PWM Timer (GPT)	Apr '11	1.0	OK
RX630	RX630	Technical Note	RX630 Hardware Manual	TN-RX*-A007A/E	Errata to List of Pins and Pin Functions (100-Pin TFLGA and 100-Pin LQFP) of RX630 Group User's Manual: Hardware	Dec '11	1.0	OK
RX62T	RX62T	Technical Note	RX62T Hardware Manual	TN-RX*-A008A/E	Permanent Countermeasure for the General PWM Timer	Jan '12	1.0	OK
RX62T	RX62T	Technical Note	RX62T Hardware Manual	TN-RX*-A009A/E	Usage Notes on the Multi-Function Timer Pulse Unit 3	Jan '12	1.0	OK
RX630	RX630	Technical Note	RX630 Hardware Manual	TN-RX*-A010A/E	Notes on Accessing the FCU RAM Area	Feb '12	1.0	OK
RX600	RX62N, RX621 RX630 , RX63N, RX631	Technical Note	many	TN-RX*-A011A/E	Usage Notes on the Multi-Function Timer Pulse Unit 2 (MTU2)	Feb '12	1.0	OK
RX600	all RX600	Technical Note	many	TN-RX*-A012A/E	I2C bus interface (RIIC) Precautions when using timeout detection function	Mar '12	1.0	OK
RX600	all RX600	Technical Note	many	TN-RX*-A013A/E	I2C bus interface (RIIC) Precautions for stop condition issuance timing when receiving master	Mar '12	1.0	OK
RX600	RX630	Technical Note	RX630 Hardware Manual	TN-RX*-A014A/E	Changes to Timing Specifications of the RIIC Module of RX630 Group	Feb '12	1.0	OK
RX200	RX210	Technical Note	RX210 Hardware Manual	TN-RX*-A015A/E	Note on Using Main Clock in RX210 Group	April '12	1.0	OK
RX200	RX210	Technical Note	RX210 Hardware Manual	TN-RX*-A016A/E	Restriction on 5V Tolerant Port in RX210 Group	April '12	1.0	OK
RX600	Rx62N, RX621 RX62T	Technical Note	RX62N Hardware Manual RX62T hardware manual	TN-RX*-A018A/E	Point for Caution When the Port Output Enable 2 and 3 Registers (POE2 and POE3) Settings Release Pins from the High-Impedance State	May '12	1.0	OK
RX600	RX62T	Technical Note	RX62T Hardware Manual	TN-RX*-A019A/E	he correction of errors in inquiry/selection host command wait state of boot mod	May '12	1.0	OK
All Rx	all RX600	Technical Note	all	TN-RX*-A020A/E	Precaution when Shifting to ROM-Read Modes	May '12	1.0	OK
RX600	RX630 Group, RX63N Group RX631 Group, RX63T Group	Technical Note	many	TN-RX*-A021A/E	Addition and Change to Specifications regarding Settings for Main Clock Oscillator and PLL Wait Control Registers and Clock Oscillation Stabilization Wait Time	July '12	1.0	OK
RX600	RX631 Group, RX63N Group	Technical Note	RX63N Hardware Manual	TN-RX*-A022A/E	Addition of Specifications regarding Sub-clock Oscillator Drive Ability Control Bi	April '12	1.0	OK

RX Family Quick Reference (Flash Programming) September-12

Product	Device	Supplier	Product	Country	Distributor	Web-site
Renesas Flash programmer	All RX	Renesas *UART* yes	 *E1 (debug)* yes			www.renesas.eu
FDT	All RX	Renesas *UART* yes	 *E1 (debug)* yes		-	www.renesas.eu
Serial Programmer	RX610, RX621 RX62N, RX62T RX210	Renesas		PG-FP5		www.renesas.eu
Serial Programmer	RX610, RX621 RX62N, RX62T *RX630*	Segger	Flasher RX	Ge		www.segger.com
EPROM Programmer	RX610 RX621,RX62N	Data I/O	Multisyte, Dual Optima, Plus48		-	http://www.data-io.com/
				Fr	M.B. Electronique	www.mbelectronique.fr
				It	ALL Data S.R.L.	www.alldata.it
				Uk	Accelonix Limited	www.accelonix.co.uk
EPROM Programmer	RX62T	System General	T9800 T9600		-	http://www.sg.com.tw/
				Fr	W-Tech	www.w-tech.fr/
				Ger	HT-Eurep Messtechnik	www.ht-eurep.de
				It	AL Electronic	www.alelectronic.it
EPROM Programmer	RX62T	Xeltek	Superpro 5000 Superpro 6000		-	www.xeltek.com
				Fr	I.S.I.T	www.isit.fr
				Ger	Hoffmann Elektronik	www.superpro.de
				It	AL Electronic	www.alelectronic.it
				Scandic	Ferner Elektronik AB	www.ferner.se
				UK	Equinox Technologies	www.equinox-tech.com
EPROM Programmer	RX610, RX621 RX62N, RX62T	Sunny Giken	Flash programmer		-	www.sunnygiken.jp/english
EPROM Programmer	RX621, RX62N RX631, RX63N	Elnec	Flash programmer			http://www.elnec.com
Serial Programmer	in request	Softec	Flashrunner			http://www.softecmicro.com/

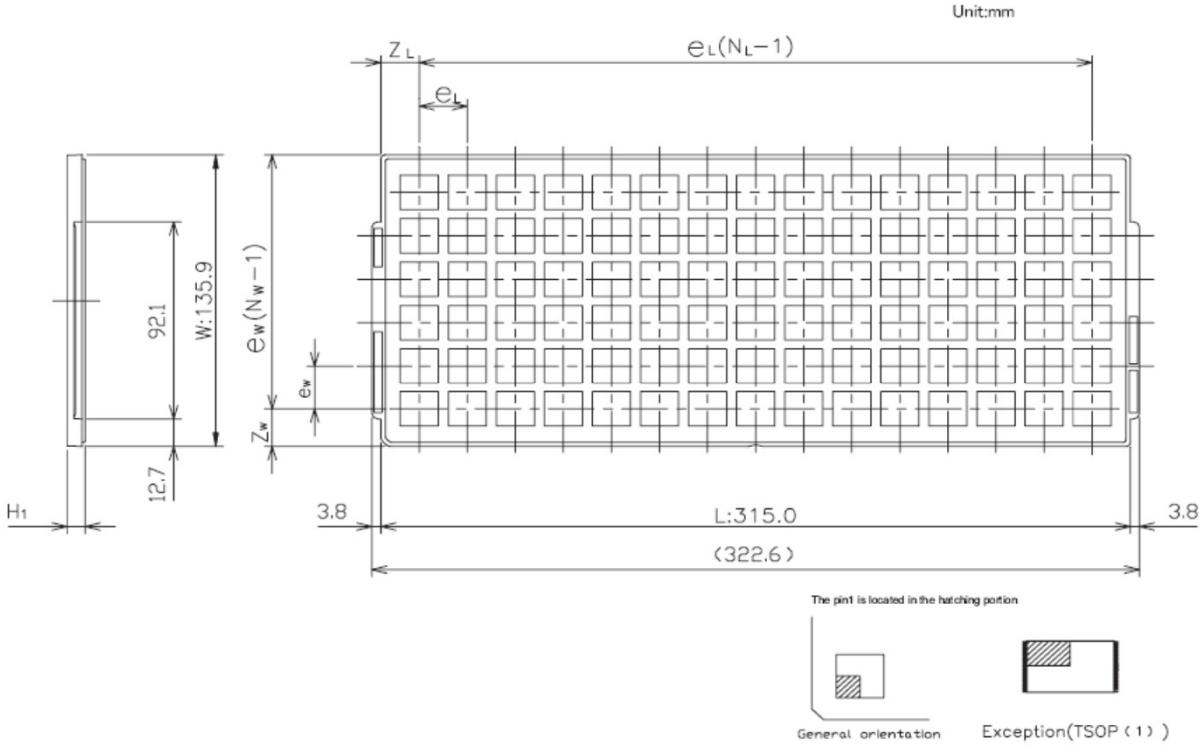
RX Family Quick Reference (Packaging) Sep-12

Package	Renesas Package Code	Device	Suffix	Package Size	Package Thickness	Tray Qty pcs	Tray Type	Box Qty pcs
LQFP-48	PLQP0048KB-A	RX63T	**FL**	7 x 7 mm ^{2} (9 x 9 mm ^{2}) * 0.5 mm pitch	1.7 mm	250	L196-56	2000
LQFP-64	PLQP0064KB-A	RX210, RX63T RX62T	**FM**	10 x 10 mm ^{2} (12 x 12 mm ^{2}) * 0.5 mm pitch	1.7 mm	160	L196-49	1280 (8 trays)
LQFP-64	PLQP0064GA-A	RX210	**FK**	14 x 14 mm ^{2} (16 x 16 mm ^{2}) * 0.8 mm pitch	1.7 mm	90	L196-05 (JEDEC)	720 (10 trays)
LQFP-80	PLQP0080KB-A	RX210	**FN**	12 x 12 mm ^{2} (14 x 14 mm ^{2}) * 0.5 mm pitch	1.7 mm	50	JHB-TQ121214-M (JEDEC)	500 (10 trays)
LQFP-80	PLQP0080JA-A	RX62T	**FF**	14 x 14 mm ^{2} (16 x 16 mm ^{2}) * 0.65 mm pitch	1.7 mm	90	L196-05 (JEDEC)	720 (8 trays)
TFLGA-85	PTLG0085JA-A	RX621	**LD**	7 x 7 mm ^{2} 0.65 mm pitch	1.2 mm	416	PTA97C (JEDEC)	3744 (9 trays)
LQFP-100	PLQP0100KB-A	RX210, RX62T RX621, RX62N RX630 RX631, RX63N	**FP**	14 x 14 mm ^{2} (16 x 16 mm ^{2}) * 0.5 mm pitch	1.7 mm	90	L196-05 (JEDEC)	720 (8 trays)
TFLGA-100	PTLG0100JA-A	RX210	**LJ**	7 x 7 mm ^{2} 0.65 mm pitch	1.05mm	416	PTA97C (JEDEC)	3744 (9 trays)
QFP-112	PLQP0112JA-A	RX62T	**FH**	20 x 20 mm ^{2} (23.2 x 23.2, mm^2) * 0.65 mm pitch	3.05 mm	60	PTA64C (JEDEC)	480 (8 trays)
QFP-144	PLQP0144KA-A	RX610 RX621, RX62N RX630 RX631, RX63N	**FB**	20 x 20 mm ^{2} (22 x 22 mm ^{2}) * 0.5 mm pitch	3.05 mm	40	PT865B	400 (10 trays)
TFLGA-145	PTLG0145KA-A	RX630 RX631, RX63N	**LK**	7x 7 mm ^{2} 0.5 mm pitch	1.05 mm	416	PTA97C (JEDEC)	3744 (9 trays)
TFLGA-145	PTLG0145JB-A	RX621, RX62N	**LE**	9 x 9 mm ^{2} 0.65mm pitch	1.2 mm	286	PTB52C (JEDEC)	2574 (9 trays)
LQFP-176	PLQP0176KB-A	RX630 RX631, RX63N	**FC**	24 x 24 mm ^{2} (26 x 26 mm ^{2}) * 0.5 mm pitch	1.7 mm	40	L196-06A (JEDEC)	320 (8 trays)
TFBGA-176	PLBG0176GA-A	RX610 RX621, RX62N RX630 RX631, RX63N	**BG**	13 x 13 mm ^{2} 0.8 mm pitch	1.2 mm	160	L196-52 (JEDEC)	1280 (8 trays)
TFLGA-177	PTLG0177KA-A	RX630 RX631, RX63N	**LC**	8 x 8 mm ^{2} 0.5 mm pitch	1.05 mm	250	FT77 (JEDEC)	2250 (9 trays)

* Values in (brackets) represents package size including pin length

RX Family Quick Reference (Tray Type) September-12

Tray Code	Package Code	Package Type	Position Dimension of Cells Z_W	Z_L	e_W	e_L	Number of Cells N_W	N_L	Thickness H_1	Max IC's per Tray	Max IC's per Box	Max Heat Resistant Temperature
L196-05	PLQP0064GA-A PLQP0080JA-A PLQP0100KB-A	LQFP-64 LQFP-80 LQFP-100	15.45	15.4	21	20.3	6	15	7.62	90	720	125 °C / 24 hr
L196-06A	PLQP0176KB-A	LQFP-176	11.25	11.1	12.6	12.2	10	25	7.62	250	2000	125 °C / 24 hr
L196-49	PLQP0064KB-A	LQFP-64	13	13.1	15.7	15.2	8	20	7.62	160	1280	125 °C / 24 hr
L196-56	PLQP0048KB-A	LQFP-48	11.25	11.1	12.6	12.2	10	25	7.62	250	2000	125 °C / 24 hr
FT77	PVQN0064LB-A	P-VQFN-64	11.25	11.1	12.6	12.2	10	25	7.62	250	2250	125 °C / 24 hr
JHB-TQ121214-M	PLQP0080KB-A	80P6Q-A	13.95	14.3	18	17.9	7	17	7.62	119	952	125 °C / 24 hr
PT865B	PLQP0144KA-A	QFP-144	TBC	TBC	TBC	TBC	TBC	TBC	TBC	TBC	TBC	TBC
PTA64C	PLQP0112JA-A	QFP-112	17.55	17.8	25.2	25.4	5	12	7.62	60	480	125 °C / 24 hr
PTA97C	PTLG0100JA-A PTLG0145KA-A	TFLGA-100 TFLGA-145	8.85	8.7	9.85	9.6	12	32	7.62	416	3744	125 °C / 24 hr
PTB52C	PTLG0145JB-A	TFLGA-145	9.7	10	11.65	11.8	11	26	7.62	286	2574	125 °C / 24 hr



Example Tray Configuration

RX Family Quick Reference (Device Sockets) September-12

Package	Renesas Package Code	Device	Socket Part Number	Supplier
LQFP-48	PLQP0048KB-A	RX63T RX210, RX220	TBC	TBC
LQFP-64	PLQP0064KB-A	RX210, RX220, RX21A RX62T, RX63T	**IC51-807,KS-12333**	**Yamaichi** **www.yamaichi.de**
LQFP-64	PLQP0064GA-A	RX210 RX62T	**IC51-0644-824-6**	**Yamaichi** **www.yamaichi.de**
LQFP-80	PLQP0080KB-A	RX210	**IC51-0804-808**	**Yamaichi** **www.yamaichi.de**
LQFP-80	PRQP0080JA-A	RX210 RX62T	**QFP11T080-008**	**Yamaichi** **www.yamaichi.de**
LQFP-100	PLQP100KB-A	RX210, RX220, RX21A RX621, RX62N RX62T, RX630 RX631, RX63N	**IC51-1004-809**	**Yamaichi** **www.yamaichi.de**
QFP-112	PLQP0112JA-A	RX62T	**TBC**	**TBC**
QFP-144	PLQP0144KA-A	RX610 RX621, RX62N RX630, RX210 RX631, RX63N	**TBC**	**TBC**
E1 Socket		All devices	**2514-6002**	**3M** **www.3m.com**

RX Family Quick Reference (Development Tools) September-12

Development Tool	Type	Device	Comment	Part Number
E100	Emulator	RX610		**TBC**
E1	**E1 debugger**	All RX	USB Version	**YR0E000010KCE00-EE**
E20	**E20 debugger**	All RX600	USB Version	**R0E000200KCT00**
RSK RX610	RX610 Evaluation kit	RX610		**R0K556100S000BE**
RSK RX62N	RX62N Evaluation kit	RX62N & RX621		**R0K5562N0S000BE**
RSK RX62T	RX62T Evaluation kit	RX62T		**R0K5562T0S000BE**
RSK RX210	RX210 Evaluation kit	RX210		**R0K505210S000BE**
RSK RX63N	RX63N Evaluation kit	RX63N & RX631	Available Early September	**R0K50563NS000BE**
RSK RX630	RX630 Evaluation kit	RX630		**R0K505630S000BE**
RSK RX63T	RX63T Evaluation kit	RX63T	Available October	**R0K50563TS000BE**
RX62N Direct Drive LCD Kit	LCD applications kit	RX62N		**YLCDRSKRX62NS**
RX62T Motor Control Kit	Motor control kit	RX62T		**YRMCKITRX62T**
Renesas Compiler	High Performance Workshop + Renesas Compiler		RX Compiler,1 user,6mth support	**YS32HEWRX-1-6**
			RX Compiler,1 user,6mth support	**YS32HEWRX-1-18**
			Annual support contract	**YS32HEWRX-SUPPORT**
			RX Compiler,1 user,network,6mth Support	**YS32HEWRX-N1-6**
			RX Compiler,1 user,network,18mth Support	**YS32HEWRX-N1-18**
			Annual support contract, network	**YS32HEWRXN-SUPPORT**
			Multi-use Licences	**Contact Renesas**

RX Family Quick Reference (Software revisions) **September-12**

Development Too	Description	Part Number	Revision	Release date
HEW	**High Performance Workshop**	**-**	**Version 4.09.01**	**Jun-11**
E100	**Emulator**	**-**		**TBC**
E1	**E1 debugger**	**R0E000010KCE00-E**	**Version 1.02**	**Mar-10**
IAR Compiler	**IAR Compiler**	**EWRX**	Version 2.30	**Jul-11**
		Please contact IAR for latest information		
Renesas Compiler	**Renesas Compiler (with HEW)**	**YS32HEWRX-1-6**	**V.1.02 Release 00**	**Oct-11**
	Renesas Compiler (with e^{2}studio)	**YRX-E2STUDIO-1U**	**V.1.02 Release 00**	**Mar-12**
FDT	**Flash Development Toolkit**	**-**	**Version 4.08 Release 02**	**July'12**

More information about our tools are available on our website at www.eu.renesas.com

RX Family Quick Reference (Oscillator suppliers) **April-11**

Oscillator Typ	Company	Website	Comment
Resonator	**Murata**	**http://www.murata.com/**	excellent oscillator matching service on web
Resonator	**Panasonic**	**http://www.panasonic.com/**	
Resonator	**Kyocera**	**http://global.kyocera.com/**	
Resonator	**TDK**	**www.tdk.co.jp**	
32 kHz crystals	**SII**	**http://speed.sii.co.jp/pub/compo/quartz/topE.jsp**	excellent oscillator matching program on web good for low power, recommended
Crystals	**Euroquartz**	**www.euroquartz.co.uk**	
Crystals	**Jauch**	**www.jauch.de**	
Crystals	**NDK**	**http://www.ndk.com**	good for 32 kHz crystals
Crystals	**Microcrystal**	**www.microcrystal.com**	good for 32 kHz crystals
Crystals	**Seiko Epson**	**http://www.epsontoyocom.co.jp/english**	

The above does not indicate a guarentee of function, each oscillator or resonator should be tested in each specific application we strongly recommend not changing resonator suppliers without carefully testing the new oscillator in any design. Many suppliers have a specific oscillator matching service and will offer advice on the correct oscillator and circuit to use with any of our devices, We strongly recommend for each design the manufacturor is contacted to check the required oscillator parameters

RX Family Quick Reference (Renesas Middleware) September-12

Product Type	Description	Web address	Status
File System	**FAT File System**	http://www.renesas.eu/products/tools/middleware_and_drivers/filesystem/a_fat/index.jsp	Available for FOC Download
Drivers	**Peripheral Driver Generator** Automatic code generation GUI	http://www.renesas.eu/products/tools/coding_tools/coding_assistance/pdg/	Available for FOC Download
	Renesas Peripheral Driver Library (RPDL) A unified API for controlling the RX peripheral modules	http://www.renesas.eu/products/tools/middleware_and_drivers/c_driver_gen/driver_gen_rpdl/index.jsp	Available for FOC Download
	USB Driver USB Host Driver, mass storage, Comm, HID Function Driver: mass storage, Comm, HID	http://www.renesas.eu/products/tools/middleware_and_drivers/c_driver/usb_driver/index.jsp	Available for FOC Download
	Serial EEPROM Driver	http://www.renesas.eu/products/tools/middleware_and_drivers/c_driver/a_eeprom_driver/index.jsp	Available for FOC Download
	Serial Flash driver	http://www.renesas.eu/products/tools/middleware_and_drivers/c_driver/a_serial_flash_driver/index.jsp	Available for FOC Download
Libraries	**TCP/IP protocol stack** Ultracompact TCP/IP protocol stack (high-speed version)	http://www.renesas.eu/products/tools/middleware_and_drivers/c_communication/a_tcp_ip/index.jsp	Available for FOC Download
	Digital Filter Library Digital Filter Library for RX600 Series	http://www.renesas.eu/products/tools/middleware_and_drivers/c_communication/a_dfl/index.jsp	Available for FOC Download
	DTMF Library	http://www.renesas.eu/products/tools/middleware_and_drivers/c_communication/a_dtmf_lib/index.jsp	Available for FOC Download
	ADPCM Voice Codec Sound Playback system library Sound compression library	http://www.renesas.eu/products/tools/middleware_and_drivers/c_communication/a_adpcm/index.jsp	Available for FOC Download
	FFT Library	http://www.renesas.eu/products/tools/middleware_and_drivers/c_numerical_lib/a_fft_lib/index.jsp	Available for FOC Download
	Signal Processing Library	http://www.renesas.eu/products/tools/middleware_and_drivers/c_splib/splib_standard/index.jsp	Available for FOC Download
Graphics	**GUI Builder**	http://www.renesas.eu/products/tools/middleware_and_drivers/c_img_processing/a_gui/index.jsp	Available for FOC Download
	2 D Graphics Library (Basic Graphics Library)	http://www.renesas.eu/products/tools/middleware_and_drivers/c_img_processing/a_glib/index.jsp	Available for FOC Download
	JPEG decoder Library	http://www.renesas.eu/products/tools/middleware_and_drivers/c_img_processing/a_jpeg/index.jsp	Available for FOC Download

These are products development under a varity of licence terms to support the RX family

Renesas makes no claim or supplies no warrenty towards the performance or the availability of these products.

RX Family Quick Reference (Ecosystem) September-12

Product Type	Product Name	Description	Supplier	Website
Compilers	IAR Compiler	C Compiler	IAR	www.iar.com
	GCC	GNU Compiler for RX	Kpit	http://www.gnuRX.com
Debuggers	JLINK	Debugger	Segger	www.segger.com
	Trace32-OCD	Debugger	Lauterbach	www.lauterbach.com
Operating Systems	µc/os	Operating System µc/os III	Micrium	www.ucos-ii.com http://www.micrium.com
		Operating System µc/os III	Micrium	www.micrium.com/page/products/rtos/os-iii
		uC probe debugger	Micrium	
	Nucleus	Operating System	Accelerated Technology	www.acceleratedtechnology.com www.mentor.com
	CMX	Operating System	CMX Systems	www.cmx.com
	osCAN	OSEK Operating System	Vector Cantech	www.vector-cantech.com
	embOS	Operating System	Segger	www.segger.com
	FreeRTOS	Operating System	FreeRTOS.org	www.freertos.org
		FreeRTOS+Trace debugger	FreeRTOS.org	http://www.freertos.org/FreeRTOS-Plus/
	Sciopta	Operating System Safety Critical	Sciopta	www.sciopta.com
	Unison RTOS	Operating System	Rowebots	www.rowebots.com
	RTXC	Operating System	Quadros	www.quadros.com
Protocol Stacks		TCP/IP, USB Host, USB Device, FileSystem,	Segger	www.segger.com
		TCP/IP stack	FreeRTOS.org	www.freertos.org
		TCP/IP, USB Host, USB Device, File System,	Micrium	http://www.micrium.com
		USB Device Stack	Thesycon	www.thesycon.com
		TCP/IP, USB device, Flash File System, CANopen	CMX	www.cmx.com
		TCP/IP, USB device, Flash File System, CANopen	IAR	www.iar.com
		TCP/IP stack	Sevenstax	www.sevenstax.com
		CANopen	Port	www.port.de
		TCP/IP, USB Host, USB Device, OTG File System, bootloader encryption	HCC-embedded	www.hcc-embedded.com
		iAnywhere Blue SDK 3.2 (Bluetooth 2.1 + EDR	ARS	www.ars2000.com
Graphics		emWIN (GUI)	Segger	www.segger.com
		GUI	Micrium	http://www.micrium.com

These are products development by various Third parties that Support the RX Family
Renesas makes no claim or supplies no warrenty towards the performance or the availability of these products.